ELECTRONIC ANALYTICAL CONTROL UNIT

TYPE 704 - VOLUME 1

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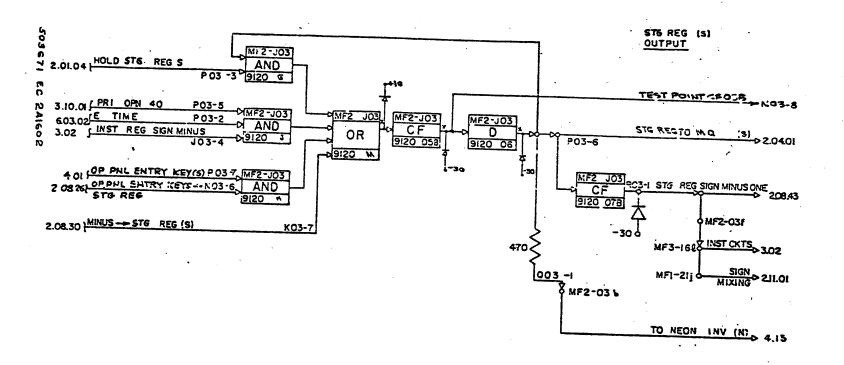
ELECTRONIC ANALYTICAL CONTROL UNIT

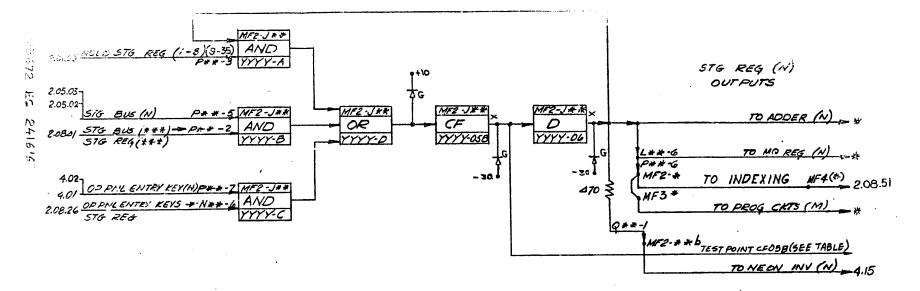
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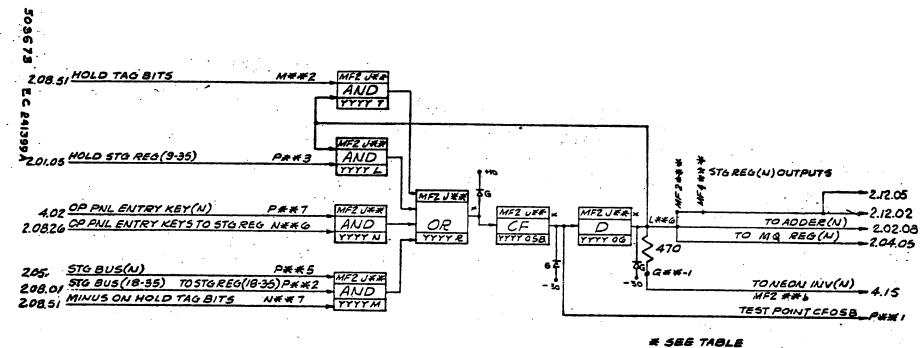
INDEX NO	DESCRIPTION	PART NO	ENG CHG
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	S, 1-35 MQ REG STORE CTRL	503757	241399A
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2.12.04	INDEX REGISTER MIXING INDEX REG TO ADDERS GATING LINE	503790	241701
2.12.05	THREY KER IN WARENS OWITHO FINE	2021.70	_ , _ 0



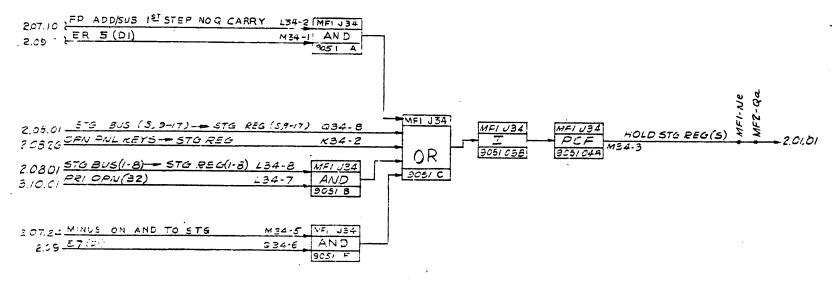


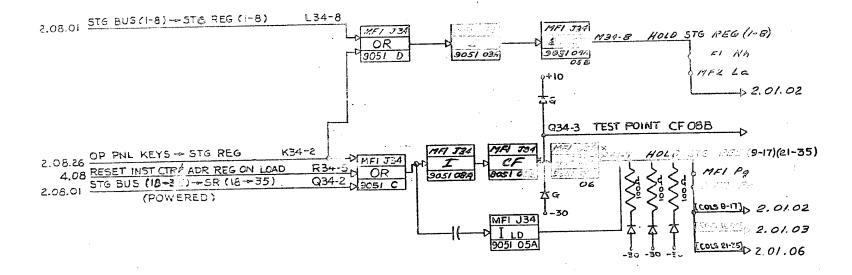
¥		NO	111	rrb	STG R	EG (N) C	OUTPUTS	,		X2
576. BUS(***) STG. REG. (***)	TEST POINT CFOSB	ARITH. COL N (N)	PLUGABLE UNIT	AND WARE UN	TO ADDERAN	TO MO REG(N)	*MF3 EOGE CONNECTOR	TO PROGRAM CIRCUITS	* MF2 EDGE CONNECTOR	*MF4 EORE CONNECTORS
1-8	P04-1	/	04	9065	2.02.02	2.04.02	16-6	3.04	04 1	14 M
1-8	P05-1	2.	05	9066		2.04.03	17-M	3.09	05 f	15 K
1-8	P06-1	3	06	9067		2.04.03	17-8	3.03	06 f	<u> </u>
1-8	P07-1	4	07	9066		2.04,03	18-d	3.03	07 f	
1-8	P08-1	5	08	9067		2.04.03	18-c	3.03	08 f	
1-8	P09-1	6	09	9066		2.04.04		3.03	09 K	
1-8	P10-1	7	10	9067		2.04.04	25-D	3.03	10 K	1
1.8	P11-1	8	11	9066		2.04,04	25-K	3.04	IIK	15 M
5,9-17	N/2-8	9	12	7611		204.05		3.04	12 K	040
5,9-17		10	13	7610		204,05		3.04	2/M	
5,9-17	N/4-8	11	14	7611		204,05		3.04	22 M	-
5,9-17	P15-1	12	15	7610	2.02.07	2.04.05		<u> </u>	↓	<u> </u>
	N16-8		16	7611		2.04.05				
S, 9-17	P/7-1	14	17	7610		2.04.05		ļ	ļ	
5,9-17	N18-8	15	18	7611		2.04.05		<u> </u>		
S, 9-17		16	19	7610		2.04.05			 	
5, 9-17	N20-8	17	20	7611	2.02.00	2.04.05	1	1		

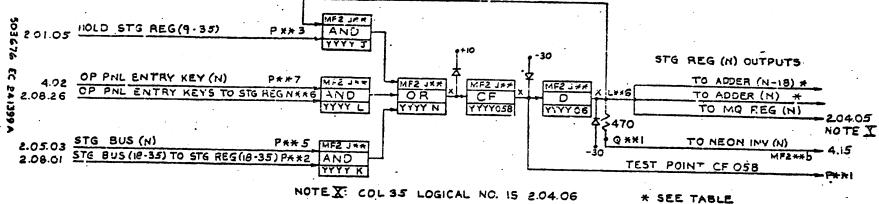
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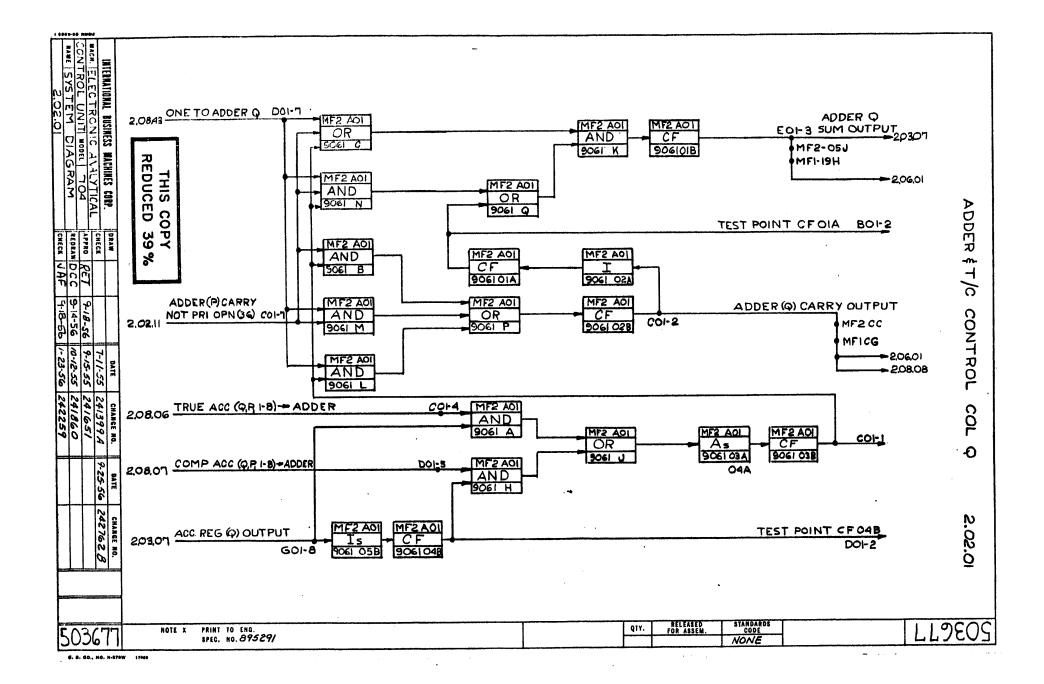
	15	F 2	WE S	WET
אפוזאכסג איי. אפודאכסג איי.	Pluggable unit	PLUGGABLE UNIT	EDGE CONNECTOR	EDGE CONNECTOR
18	9180	2/	24F	2514
19	9/79	22	25 F	2714
20	9180	23	2: F	28%

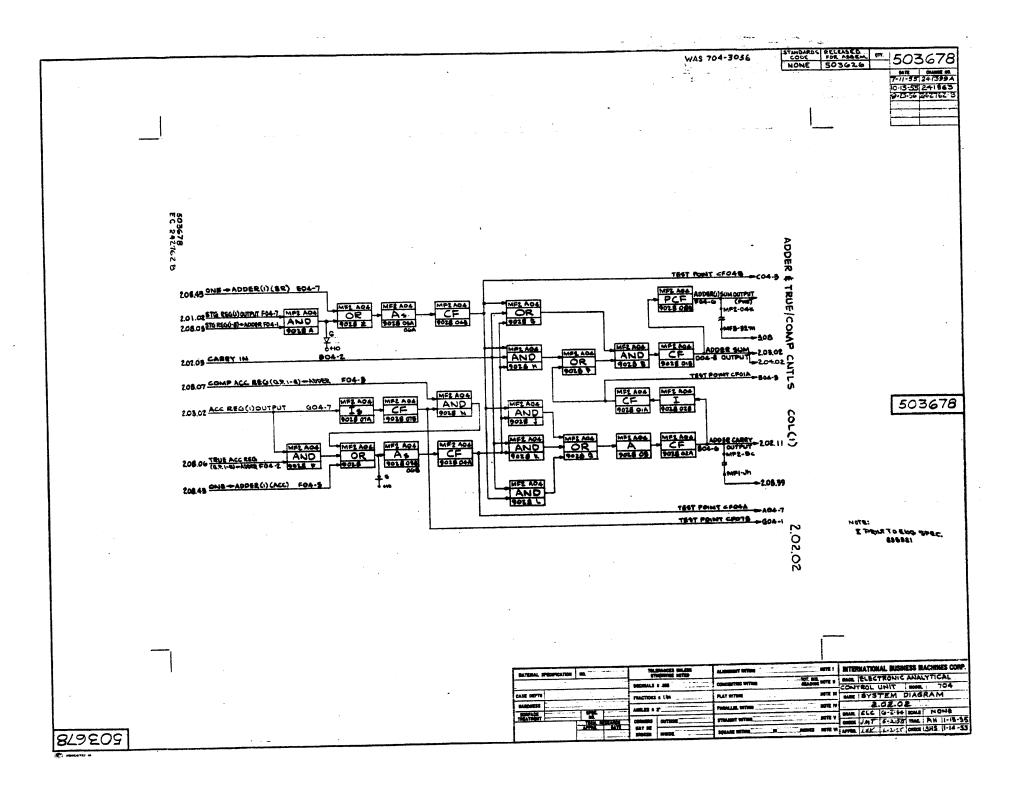


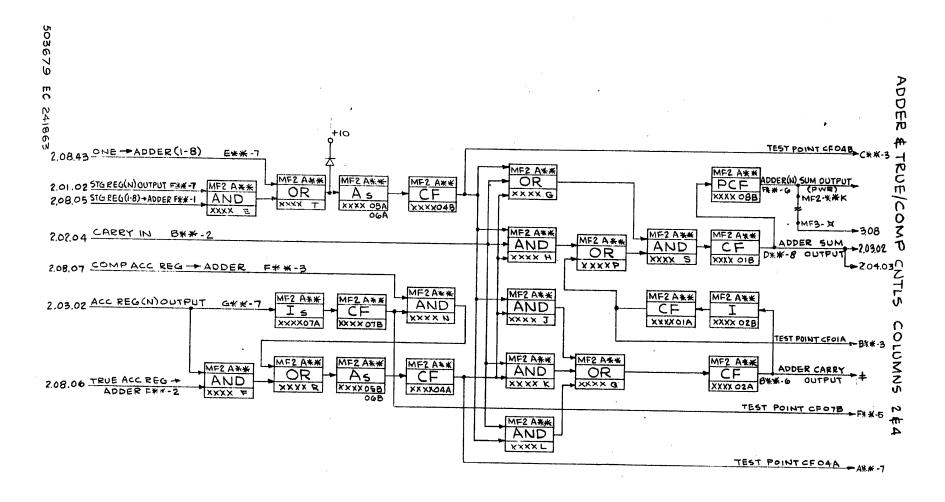




	Ec	E ?	STG REG	STG REG
Ö	3 SN	1 (* *	(N) OUTPUT	
ARITH COL (N)	PLUGGABLE UNI PART NO (YYYY)	P.UGGABLE U	TO ADDER(N) LOGICAL DIAGRAM NO	TO ADDER (N-18) LOGICAL DIAGRAM NO
15	9182	24	2.02.09	
22	9181	25	2.02.08	
23	9182	26	2.02.08	
24	9181	27	2.02.08	2.02.05
25	9182	28	2.02.09	2.02.05
26	9181	29	2.02.08	2.02.05
27	9182	30 .	2.02.08.	2.02.06
28	9181	3!		2.02.07
29	9182	32	2.02.09	2.02.07
30	9181	33	2.02.08	2.02.07
31	9182	34	80.50.5	2.02.06
32	9181	3 <i>5</i>	2.02.08	2.02.07
33	9182	36	2.02.09	2.02.10
34	9181	37	2.02.08	2.C2.10
35	9060	38	2.02.08	2.02.05

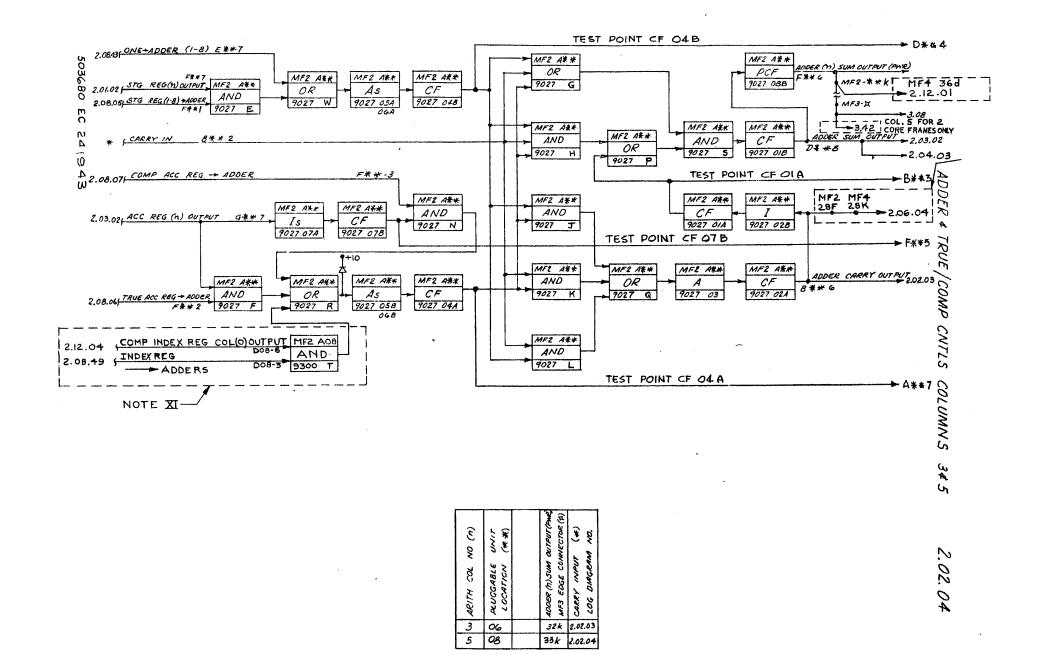


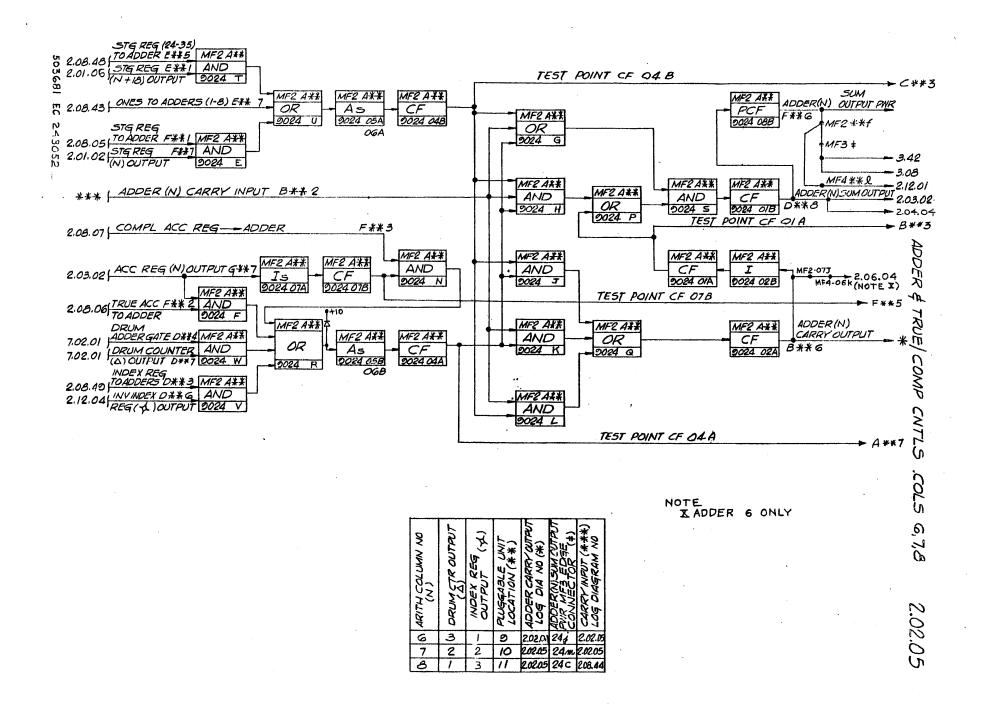


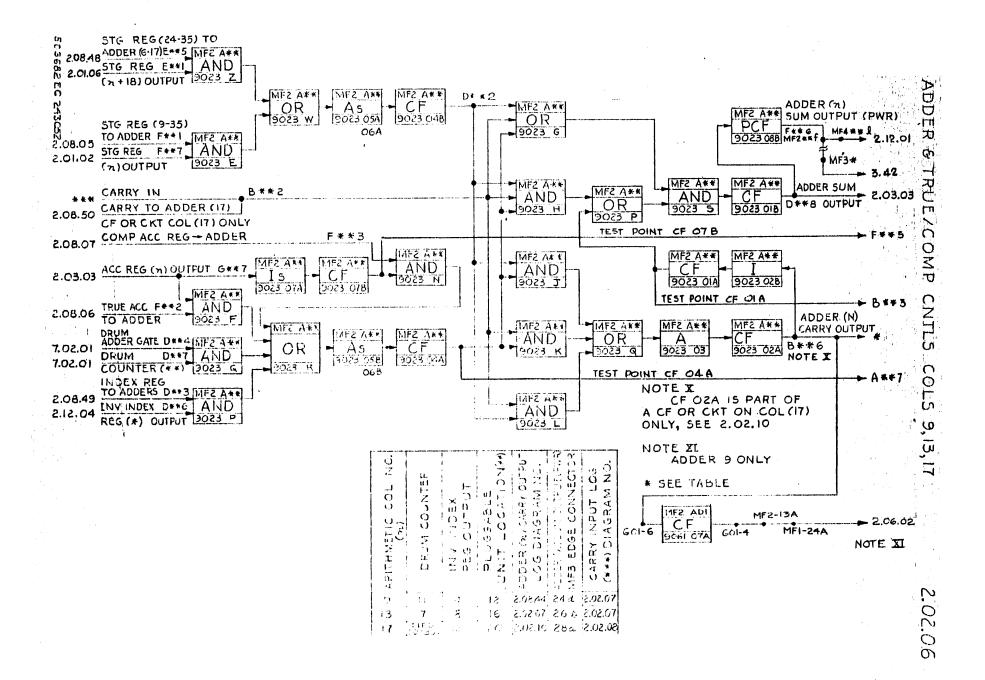


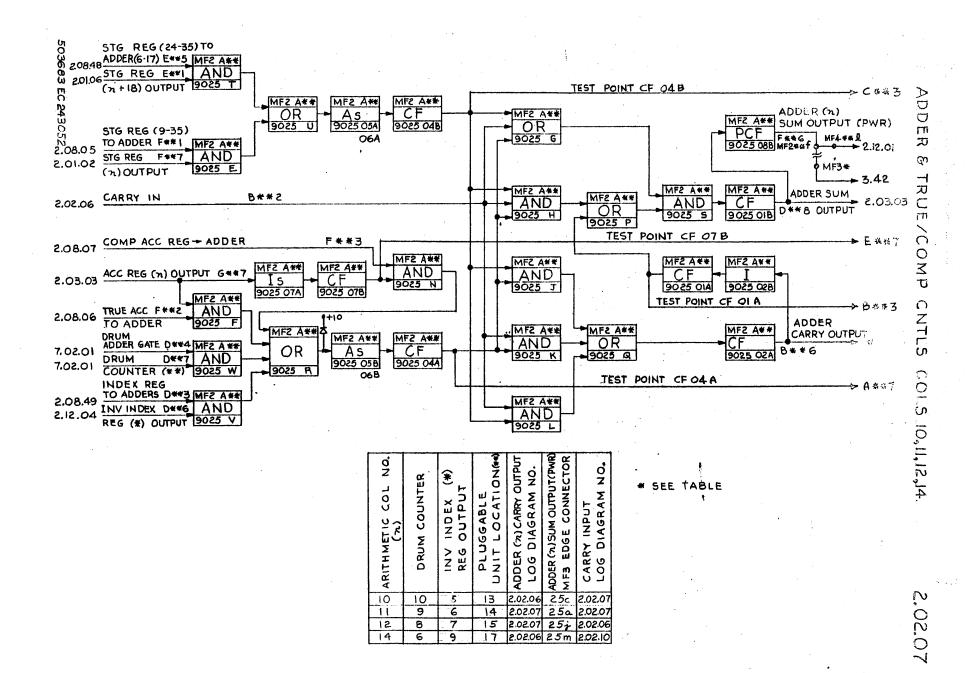
ARITH, COLUMN NO. (N)	2	4
PLUGGABLE UNIT	05	07
ADDER CARRY OUTPUT LOG. DIAGRAM NO(4)		2,02.04
ADDER(N) POWER OUTPUT MF3 EDGE CONN.(X)		33 L
PU NUMBER XXXX	9026	9177

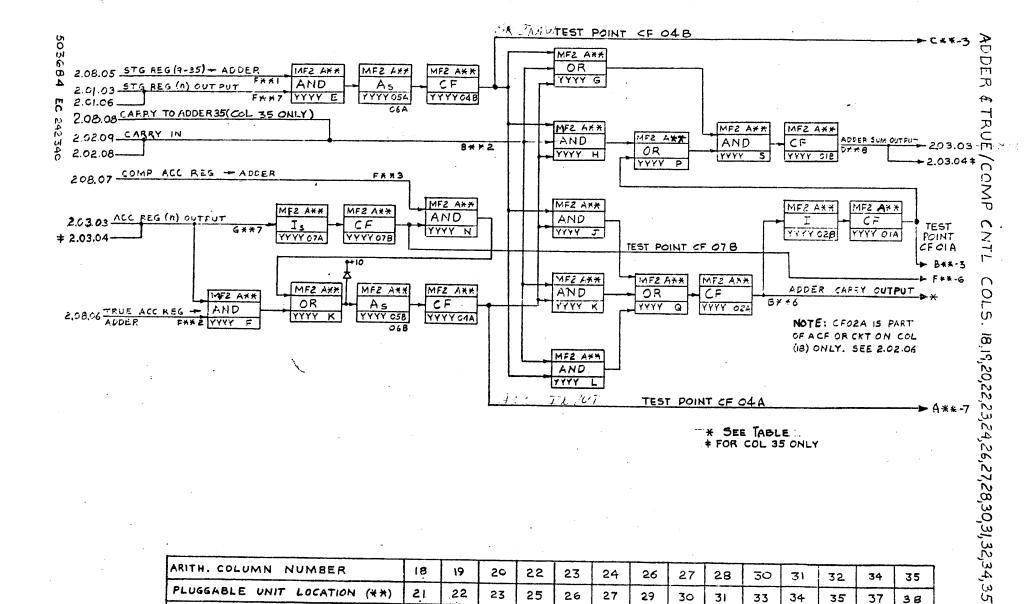
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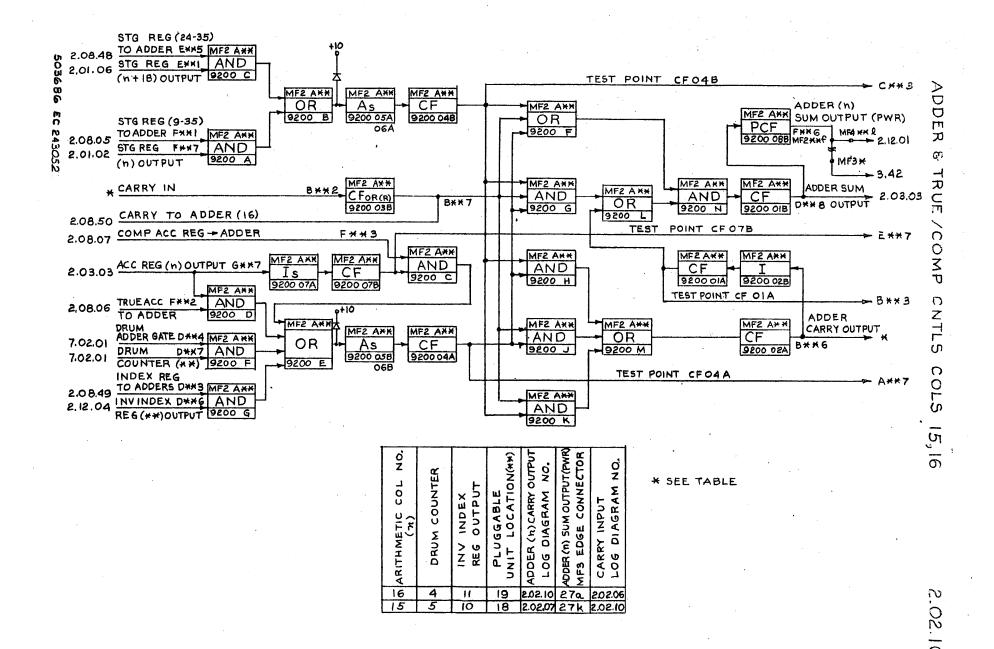


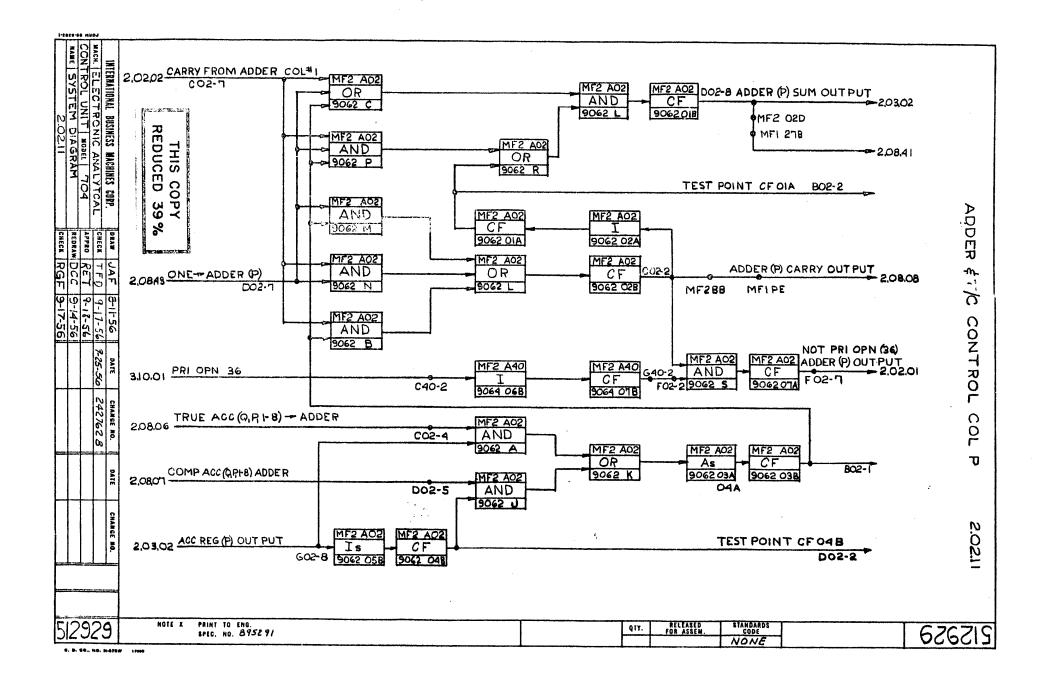




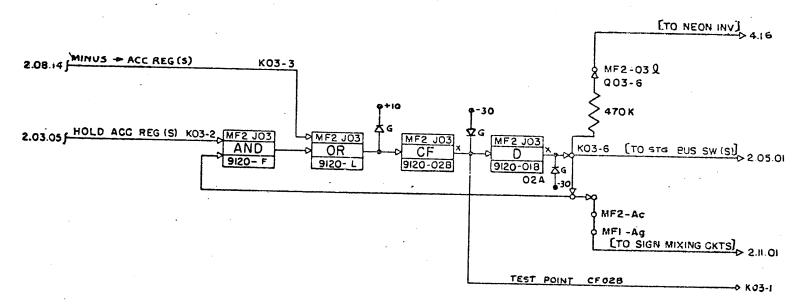
ARITH. COLUMN NUMBER	18	19	20	22	23	24	26	27	28	30	31	32	34	35
PLUGGABLE UNIT LOCATION (+*)	21	.22	23	25	26	27	29	30	31	33	34	35	37	38
PLUGGABLE UNIT PART NO (YYYY)	9176	9176	9176	9170	9170	9170	9170	9170	9170		9170			
ADDER CARRY OUTPUT (X) LOG. DIAGRAM NUMBER														

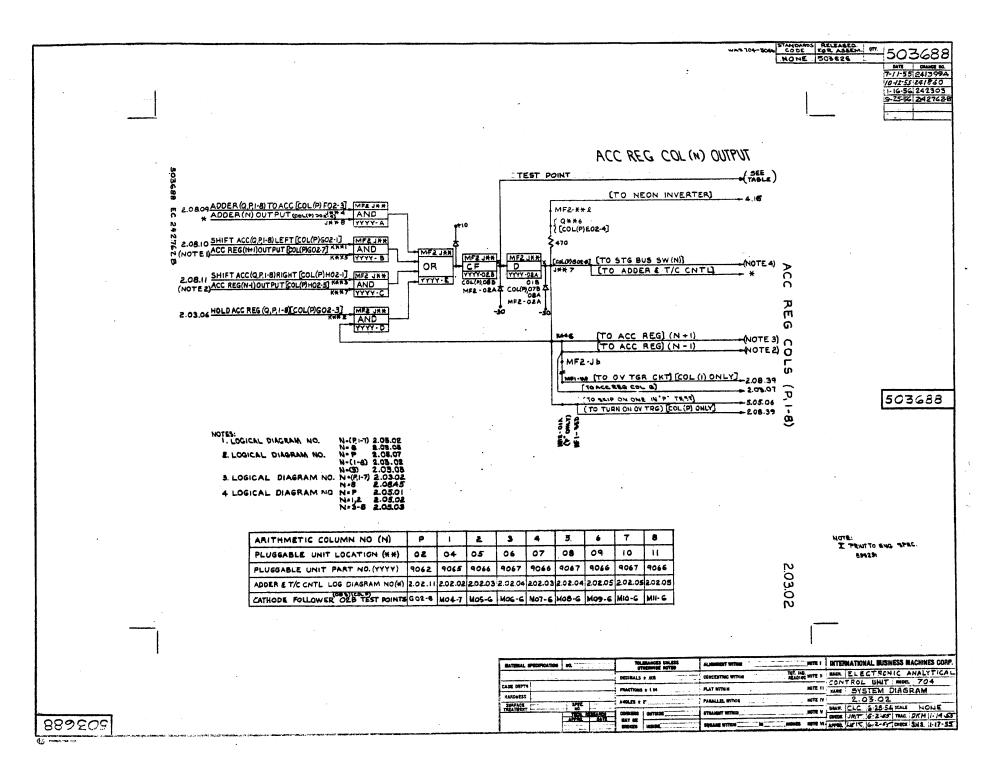
ARITH COLUMN NUMBER(N)	21	25	29	33
PLUGGABLE UNIT LOCATION(**)	_ 24,	28	32	36
PLUGGABLE UNIT PART NO (YYYY).	9171	9171	9171	9171

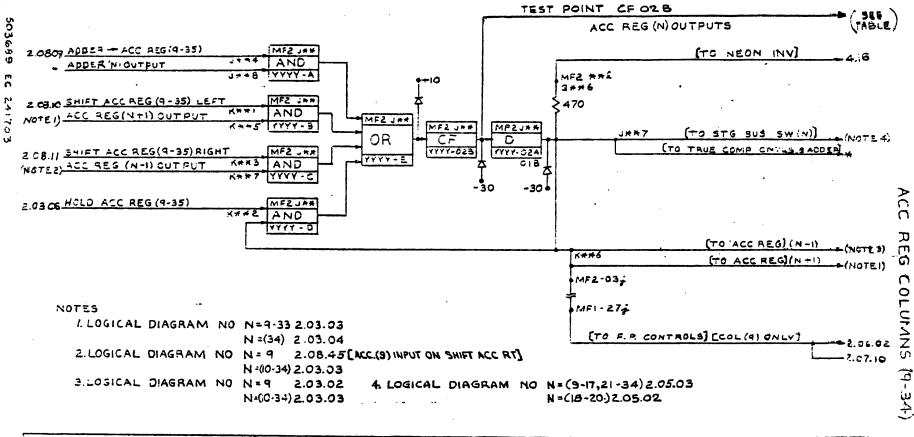






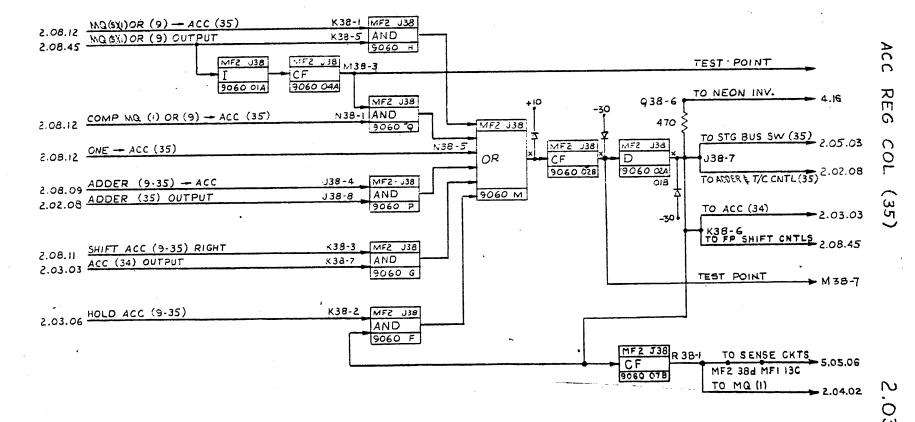


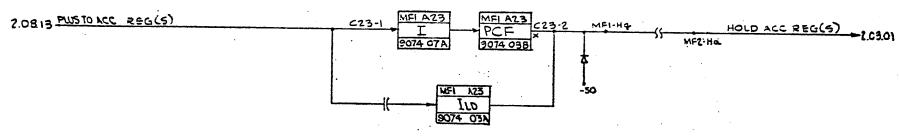


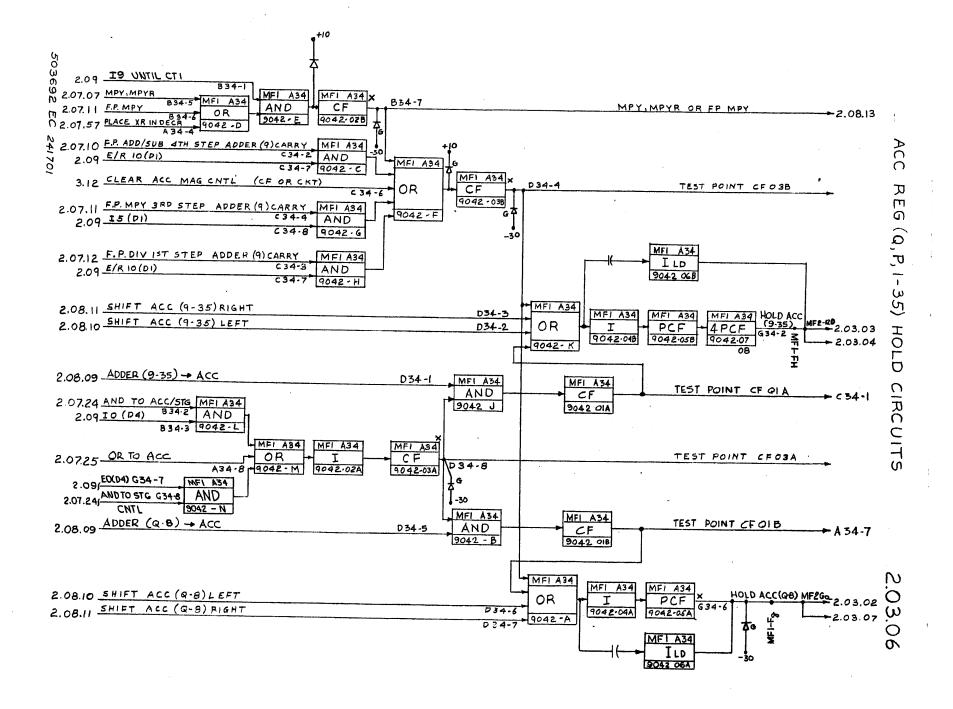


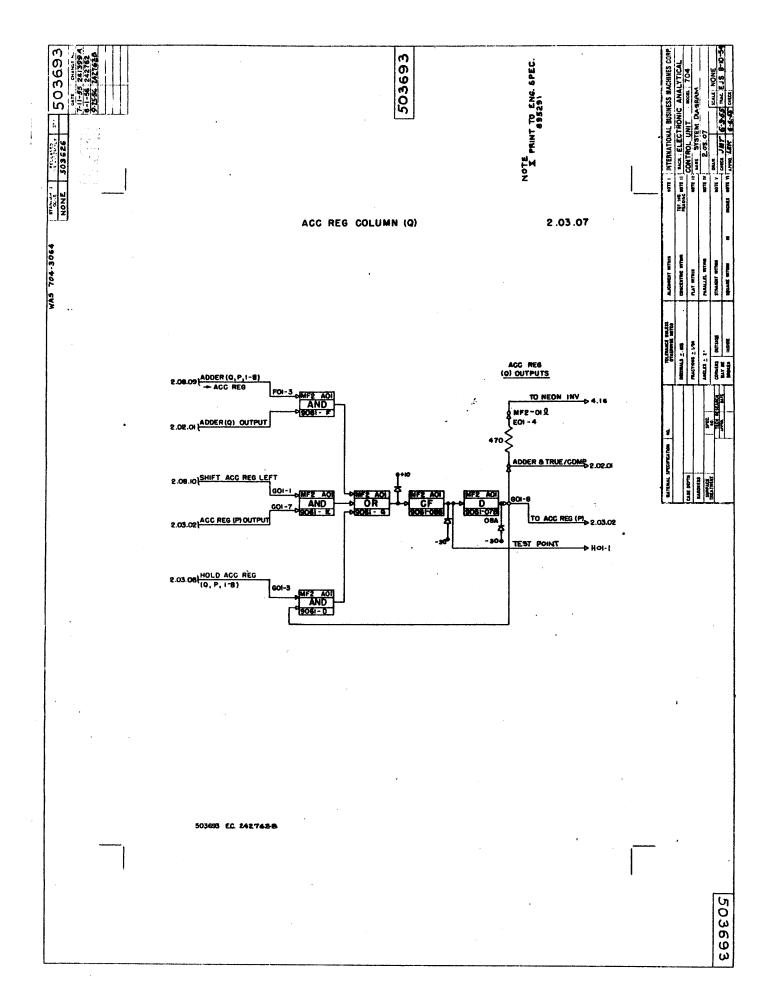
TEST POINT OF 028	M12-6	M13-6	M14-6	M15-6	M16-6	M17-6	M18-G	M19-6	M20-6	M21-7	M22-6	M23-7	M24-6	H 25.7
ARITHMETIC COLUMN NO (N)	9	10	11	12	13	14	15	i 5	17	18	19	20	21	22
PLUGGABLE UNIT LOCATION (4%)	12	13	14	15	16	17	18	19	20	21	22	23	24	25
FLUGGABLE UNIT PART NO(YYYY)	7611	7610	7611	7510	7611	7610	7611	7610	7611	9180	9179	9180	9182	9181
TO ADDER ET/C CHTLS LOS DIA NO(4)	2.02.06	2.02.07	2.02.07	2.02.07	2.02.06	2.02.07	2.02.10	202.10	2.02.06	2.02.08	2.02.08	2.02.08	202.09	202.05
ARITHMETIC COLUMN NO (N)	23	24	25	26	27	28	29	30	31	32	33	34		-
PLUGGABLE UNIT LOCATION (**)	26	27	28	29	30	31	32	33	34	35	36	37		
PLUSSABLE UNIT PART NO(YYYY)	9182	9181	9182	9181	9182	9181	9182	9181	9162	9181	9182	9181		
TO ADDER E TIC ENTLS LOGDIA. NO.(*)	2.02.08	20208	202.09	2.02.08	80505	2.02.08	2.02.09	2.02.08	2.02.08	20208	2.02.09	2.02.08	<u> </u>	
TEST POINT CF 02B	M26-6													 -

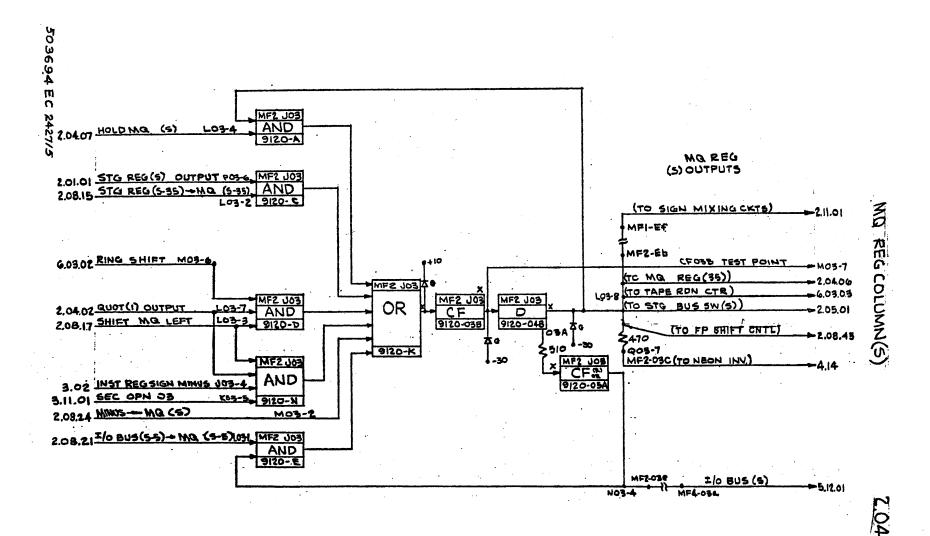
2.03.03

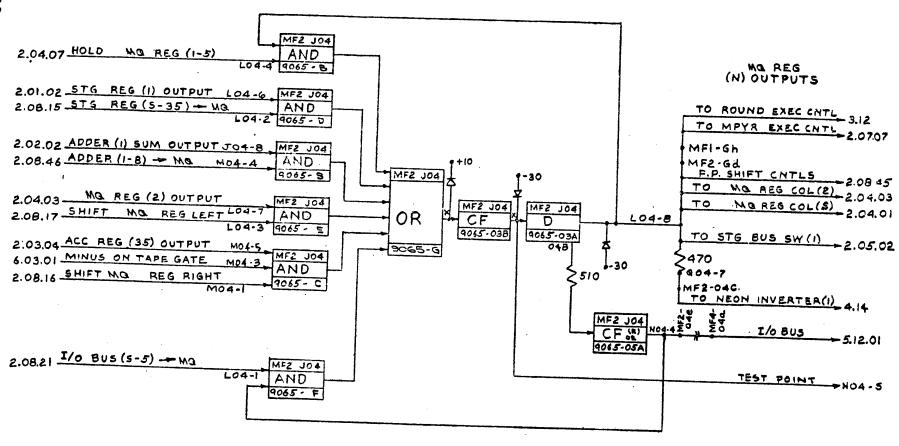












MF2 J** AND YYYY-E

1, N=2,3,4 LOGICAL NO.152.04.03 N=5 LOGICAL NO.15 2.04.04 2. N = 2 LOGICAL NO. 15.2.04.02

110 BUS (5.5) -- MG

NOTES:

NUMBERS (N) PLUGGABLE 05 06 07 UNIT LOC.(**) N=34,5 LOGICAL NO.15 2.04.03 PLUGGABLE UNIT PART NO. (YYYY) 9066 9067 9067 ADDER (N) SUM |2.02.03|2.02.04|2.02.03|2.02.04 20502 205.03 2.05.03 2.05.03

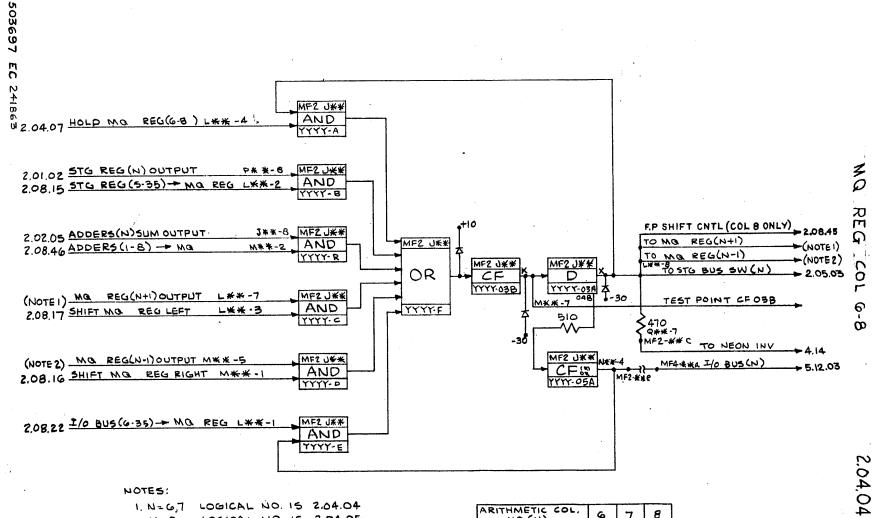
ARITH, COLUMN

* SEE TABLE

m

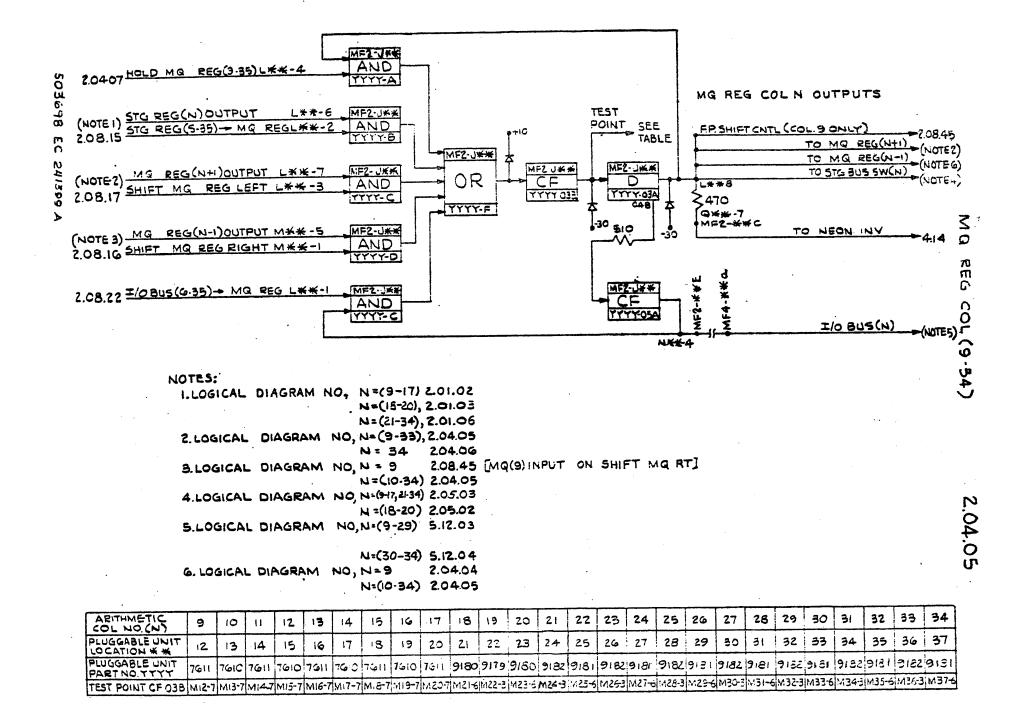
GISTER

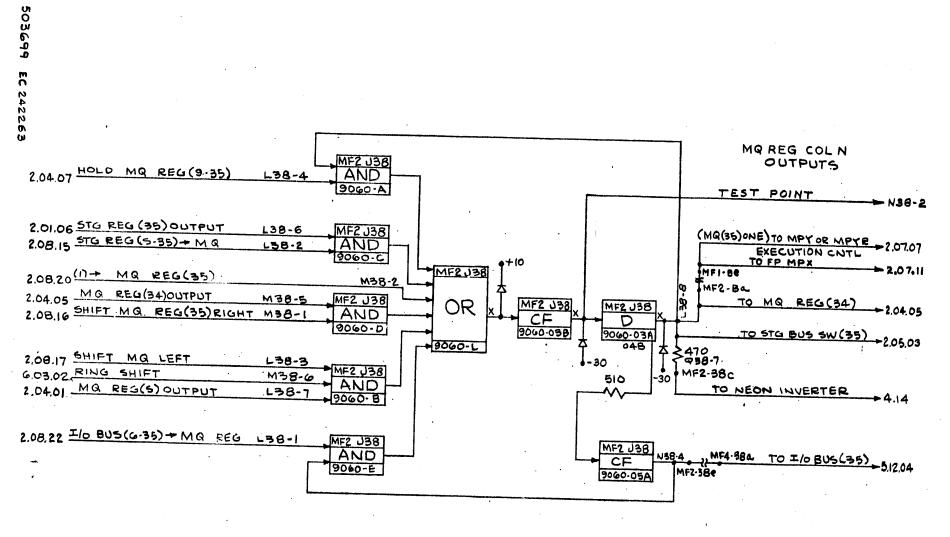
COLUMNS (2-5)

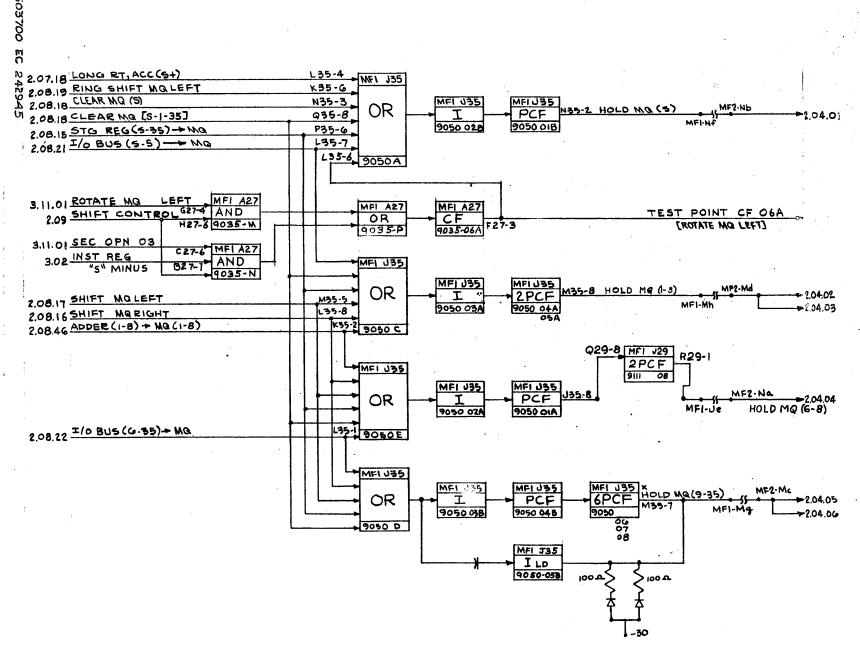


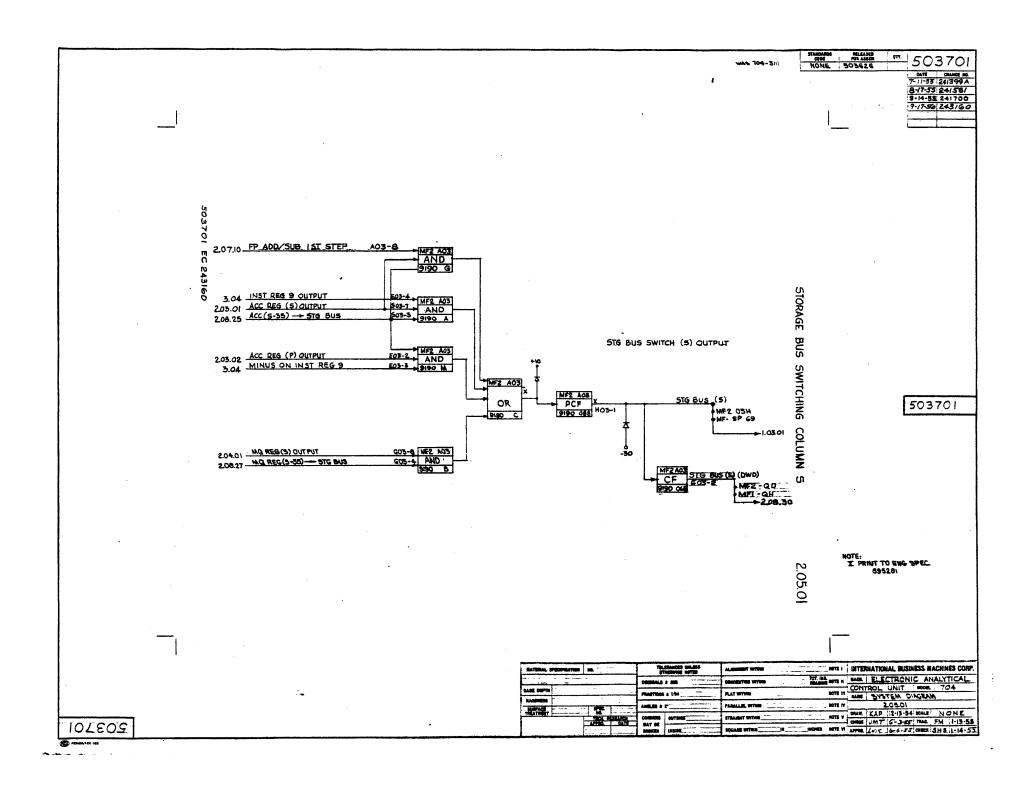
1. N=6,7 LOGICAL NO. 15 2.04.04 N=8 LOGICAL NO. 15 2.04.05 2. N=6 LOGICAL NO. 15 2.04.03 N=7,8 LOGICAL NO. 15 2.04.04

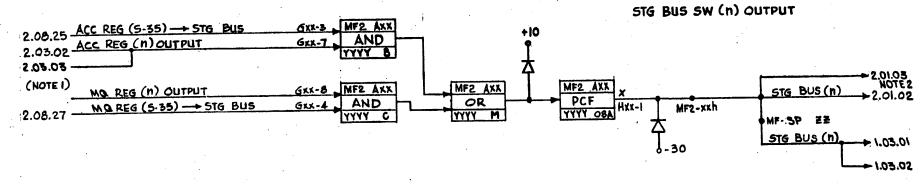
ARITHMETIC COL.	G	7	8
PLUGGABLE UNIT	Ø	0	Ξ
PLUGGABLE UNIT PART NO. (YYYY)	9066	9067	9066











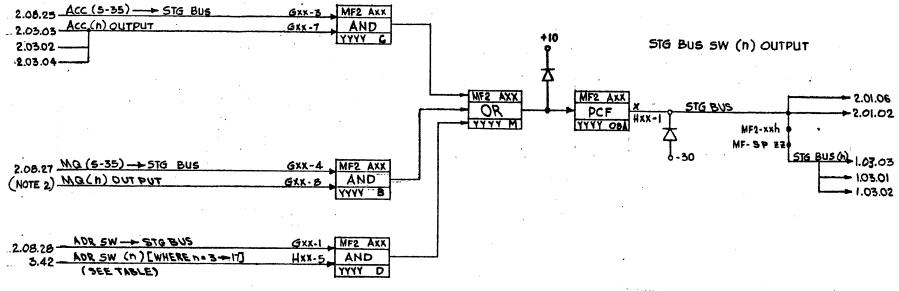
ARITH COL NO (n)	PLUGGABLE UNIT	PLUGGABLE UNIT	CONNECTOR MF = SP I(ZZ)
Ī	04	9028	85
2	05	9026	101
18	21	9176	76
19	05 21 22	9176	76 92
18 19 20	23	9176	108

NOTE 1: COL (1) SYSTEMS DWG 2.04.02

COL (2) SYSTEMS DWG 2.04.03

COL (18-20) SYSTEMS DWG 2.04.05

NOTE 2: FOR 18,18 & 20 ONLY



	ARITH COL NO	PLUGGABLE UNIT. LOCATION (xx)	PLUGGABLE UNIT PART NO (YYYY)	CONNECTOR MF-SP I (ZZ)	ADR SW (N)	ARITH COL NO (n)	PLUGGABLE UNIT LOCATION (XX)	PLUGGABLE UNIT PART NO (YYYY)	CONNECTOR MF. SP (ZZ)	ADR SW (N)
1	3	06	9027	117	NOTE 3	21	24	9171	124	NOTE 3
	4	07	9177	133	NOTE 3	22	25	9170	140	NOTE 3
L	_ 5	08	9027	149	NOTE 1	23	26	9170	128	NOTE !
L	6	09	9024	78	6	24	27	9170	65	6
L	7	10	9024	94	7	25	28	9171	81	7
L	8		9024	110	6	26	29	9170	97	8
L	9	12	9023	126	9	27	30	9170	113	9
	10	13	9025	142	10	2.8	31	9170	129	10
L	- 11	14	9025	119	11 -	29	32	9171	144	11
L	12.	15	9025	67	12	30	33	9179	74	12
L	13	16	9023	83	13	31	34	9170	90	13
L	14	17	9025	99	14	32	35	9170	106	14
L	15	18	9200	115	15	33	36	9171	122	15
L	16	19	9200	131	16	34	37	9170	138	16
L	17	20	9023	147	17	35	38	9170	135	17

NOTE 1: ARITH COL 5 IS WIRED TO ADR SWS

FOR 2 CORE FRAME OPN (-30 REMOVED)

NOTE 2: COL (3-5) SYSTEM DWG. 2.04.03 |

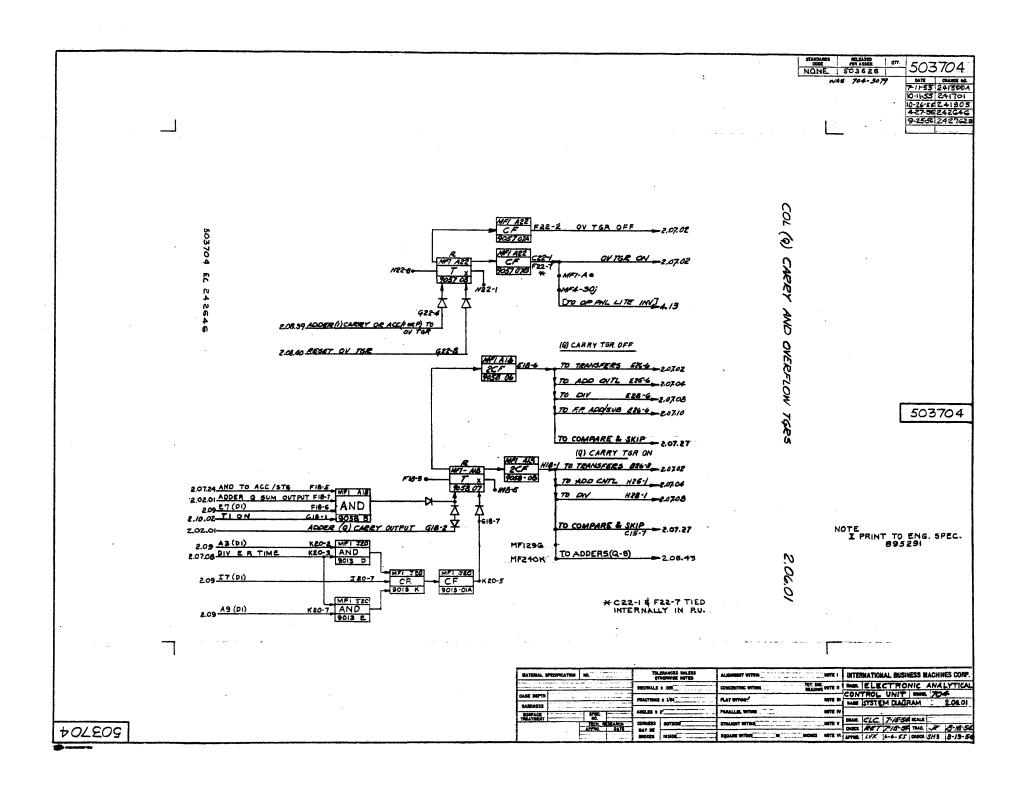
COL (6-8) SYSTEM DWG. 2.04.04 |

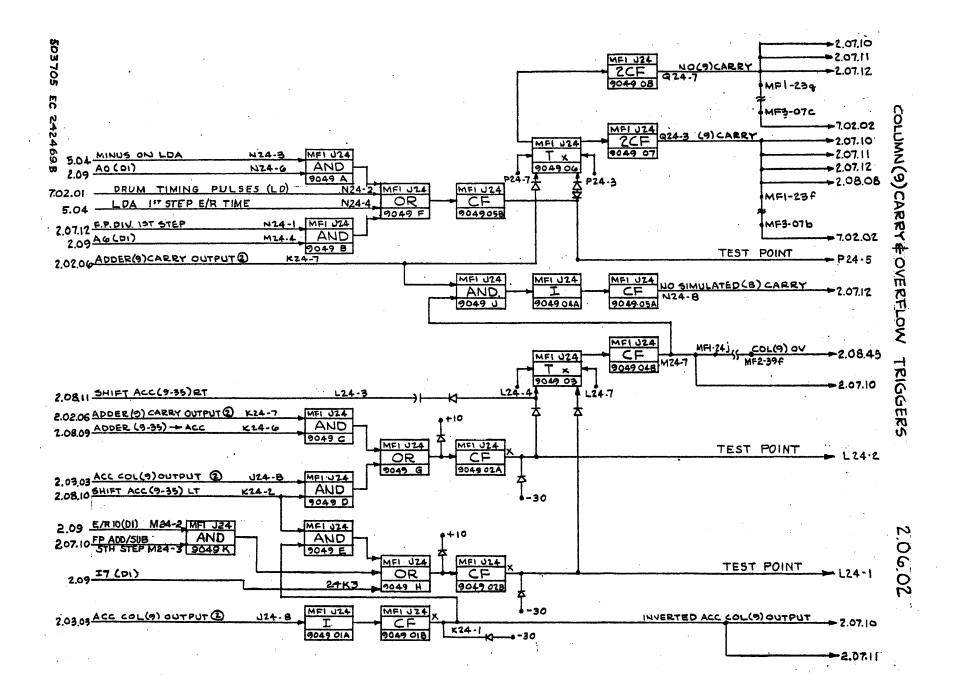
COL (9-17) SYSTEM DWG. 2.04.05 |

COL (21-34) SYSTEM DWG. 2.04.05 |

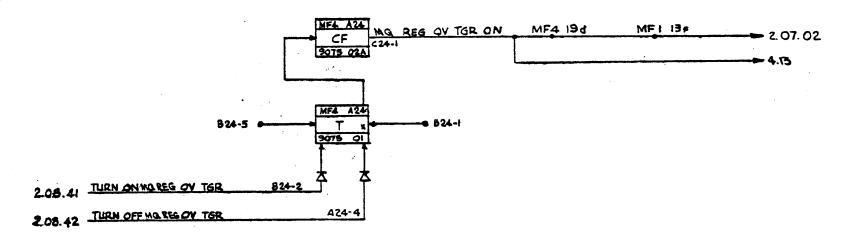
COL (35) SYSTEM DWG. 2.04.06 |

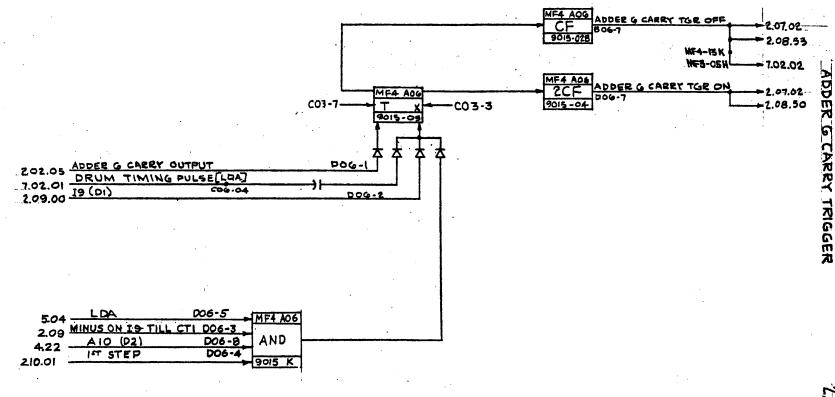
NOTE 3: HXX5 CONNECTED TO -30 FOR COL (3-5) \$ (21-23)



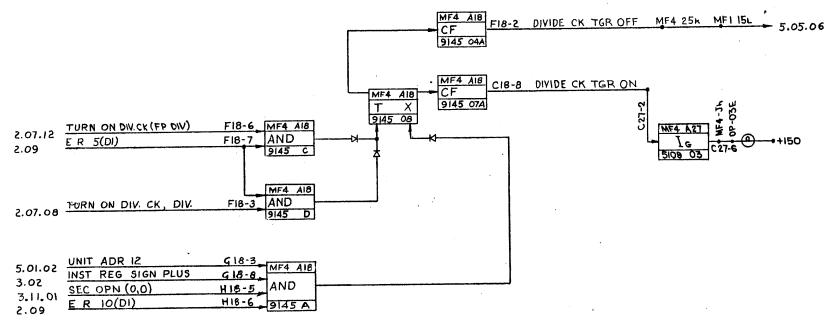


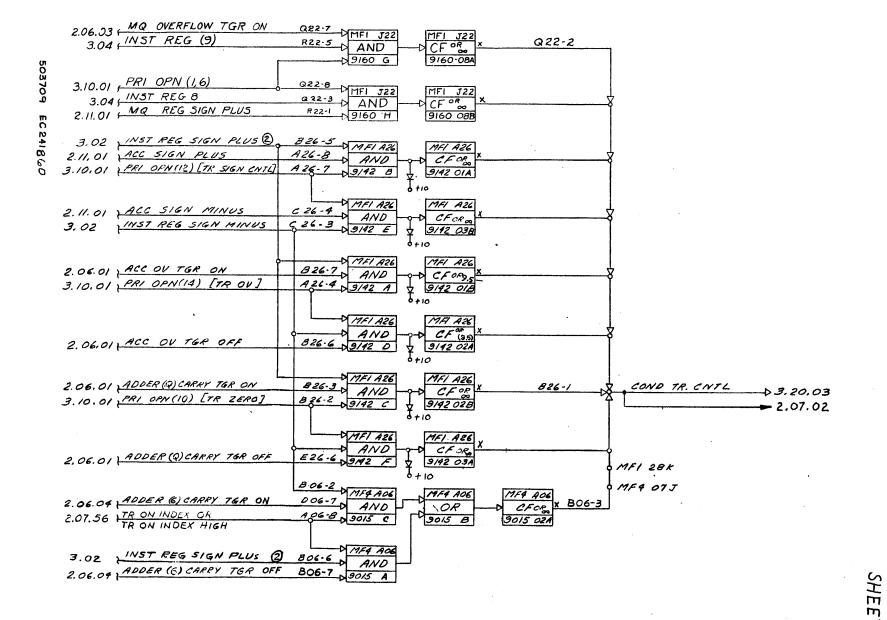




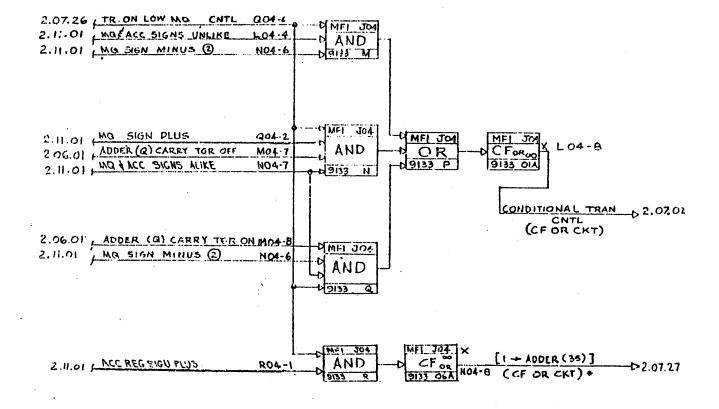


06.04

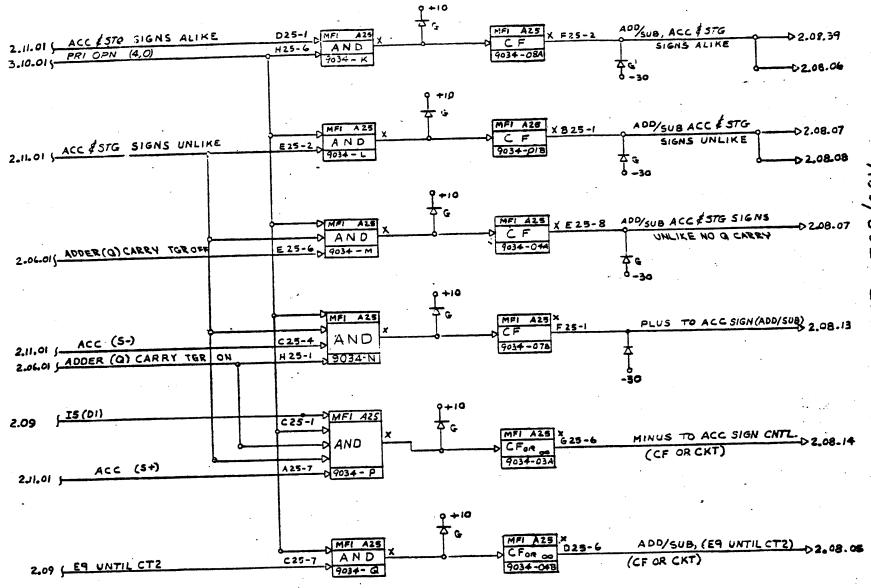


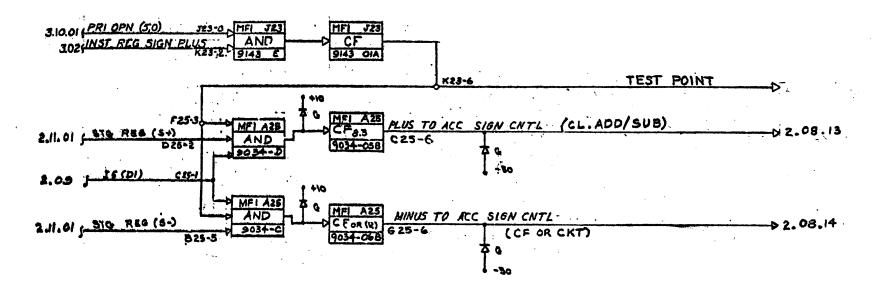


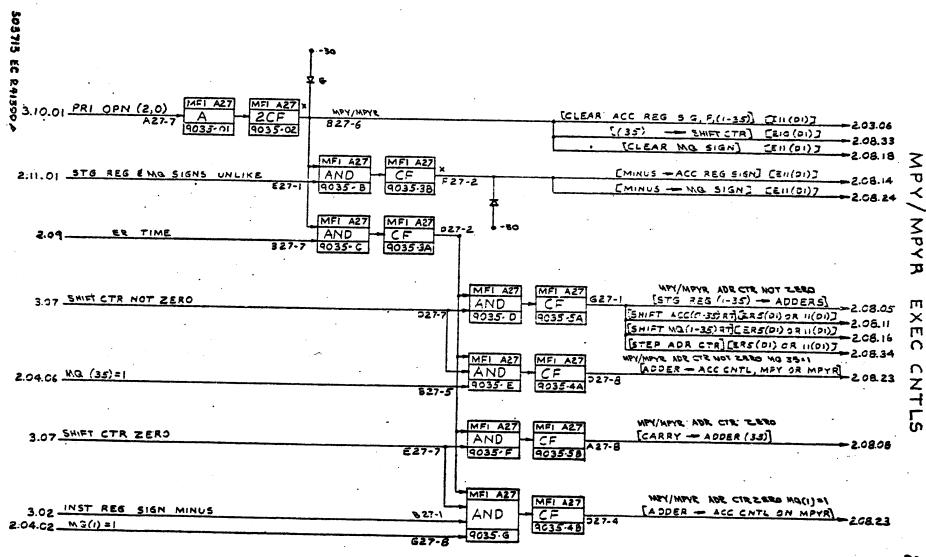
CONDITIONAL TRANSFER CNTL 2.07.02 (TR ON LOW MQ) SHEET 20F2



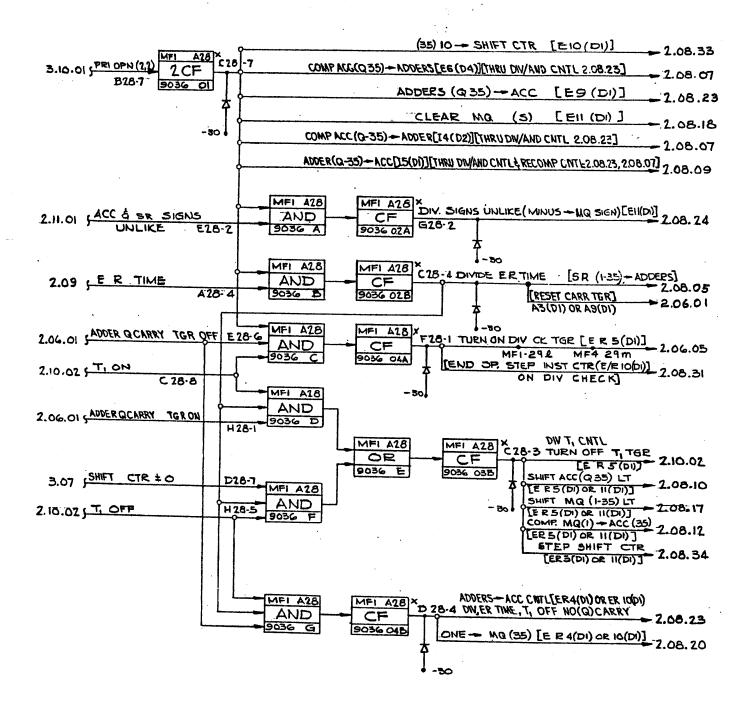
* OTHER HALF IN 9166 - CF 04 B

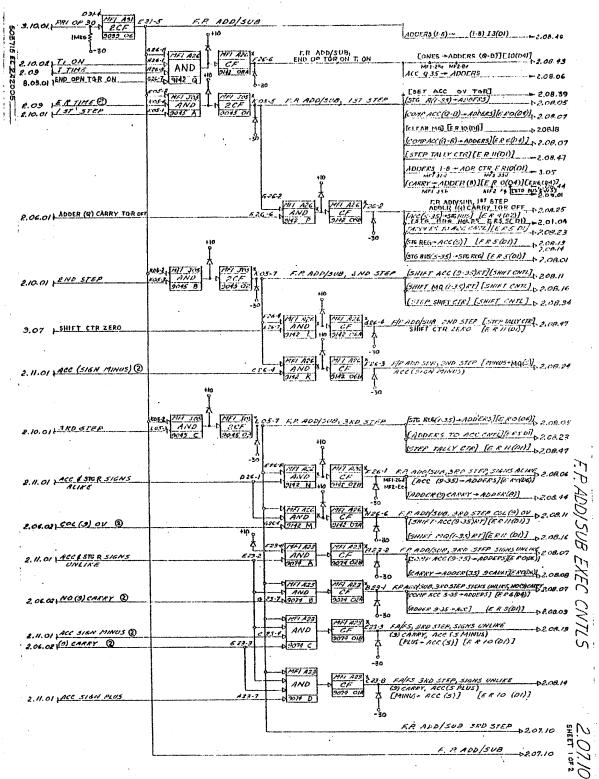


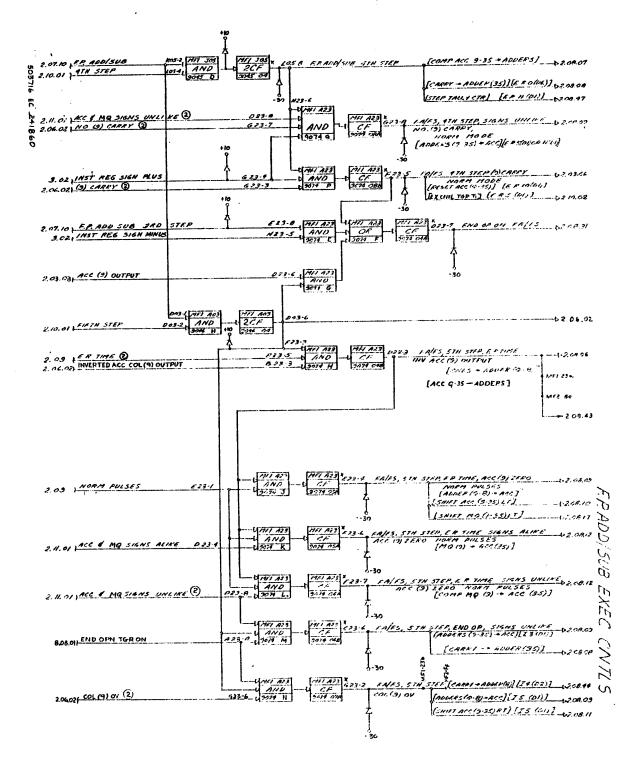




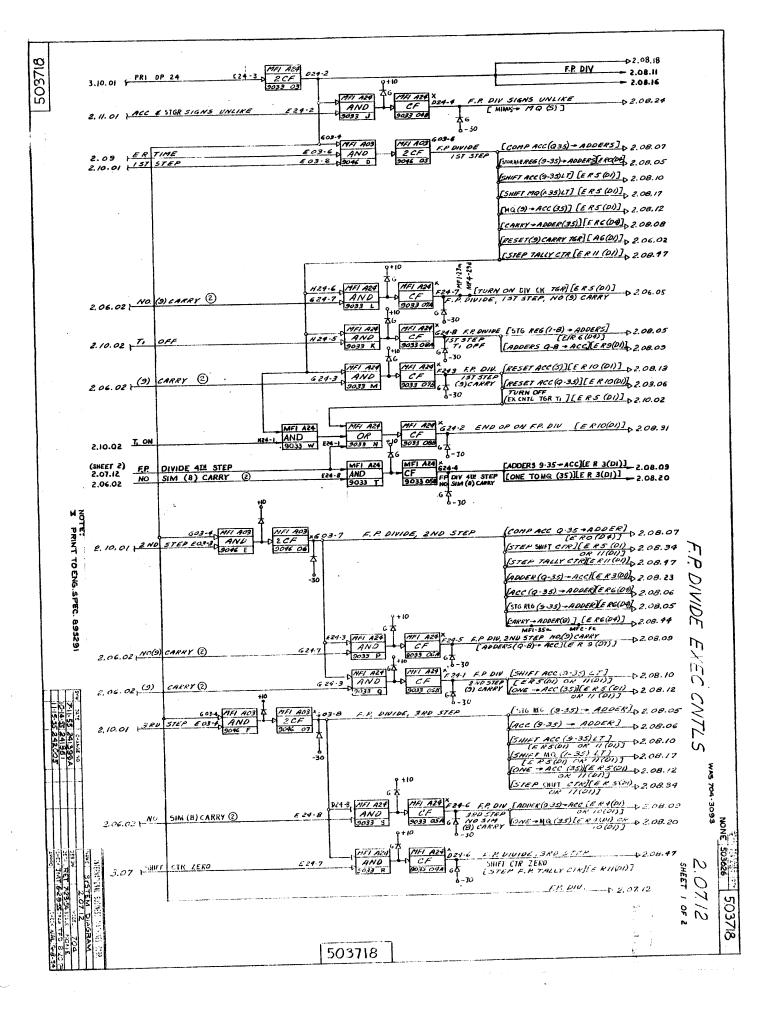
2.07.07

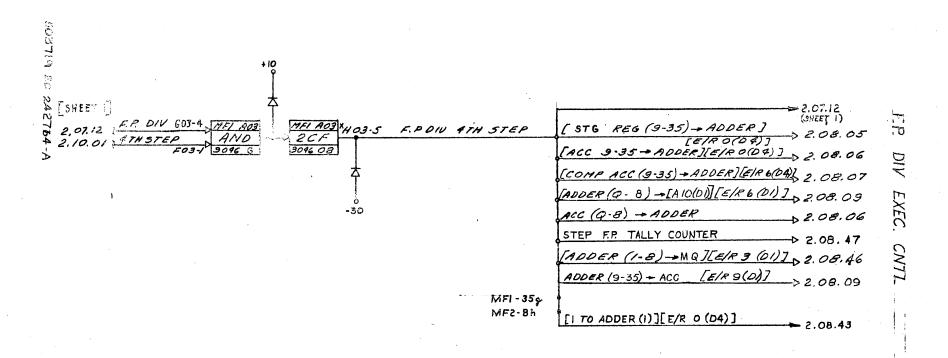


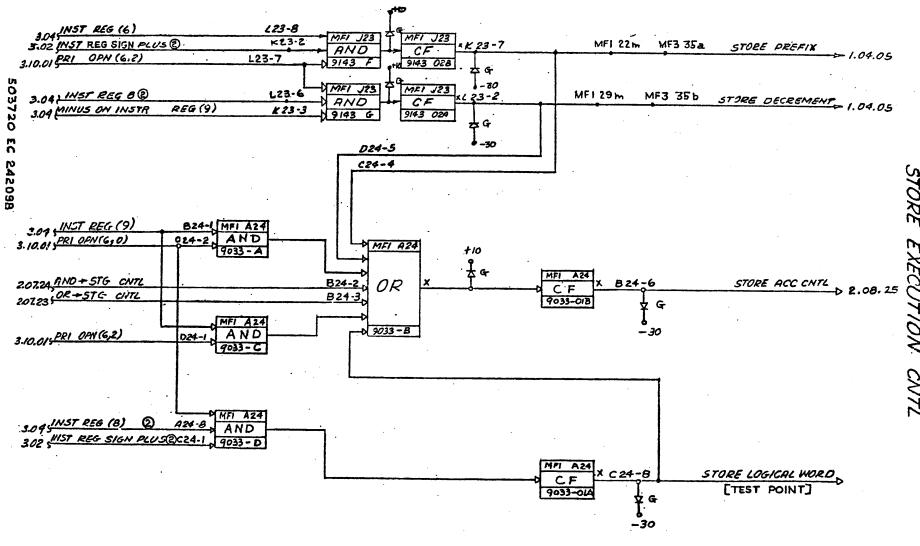




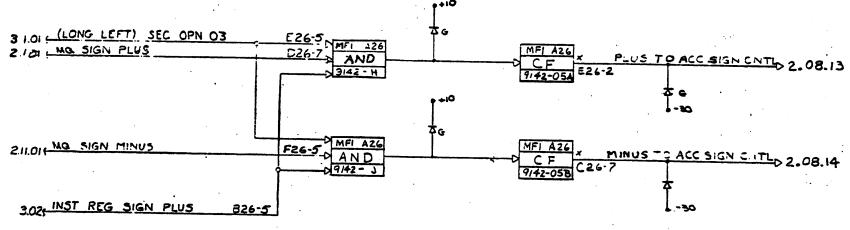
503717

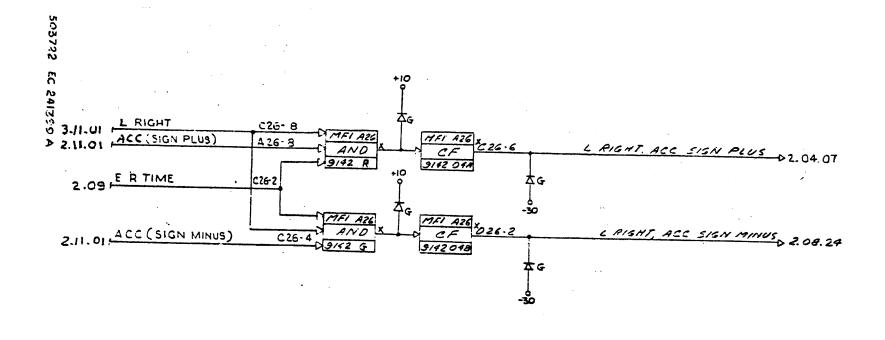


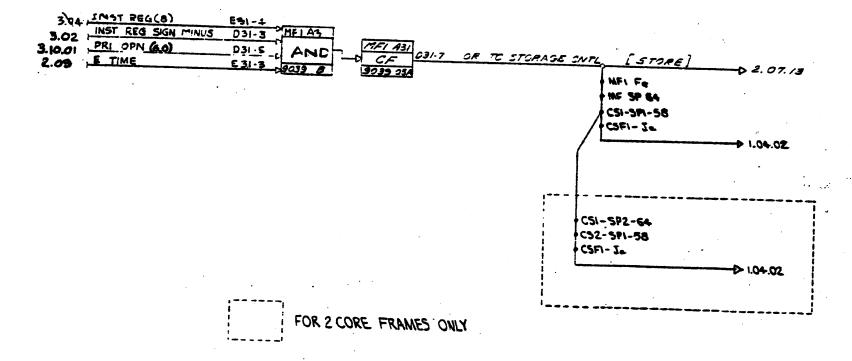


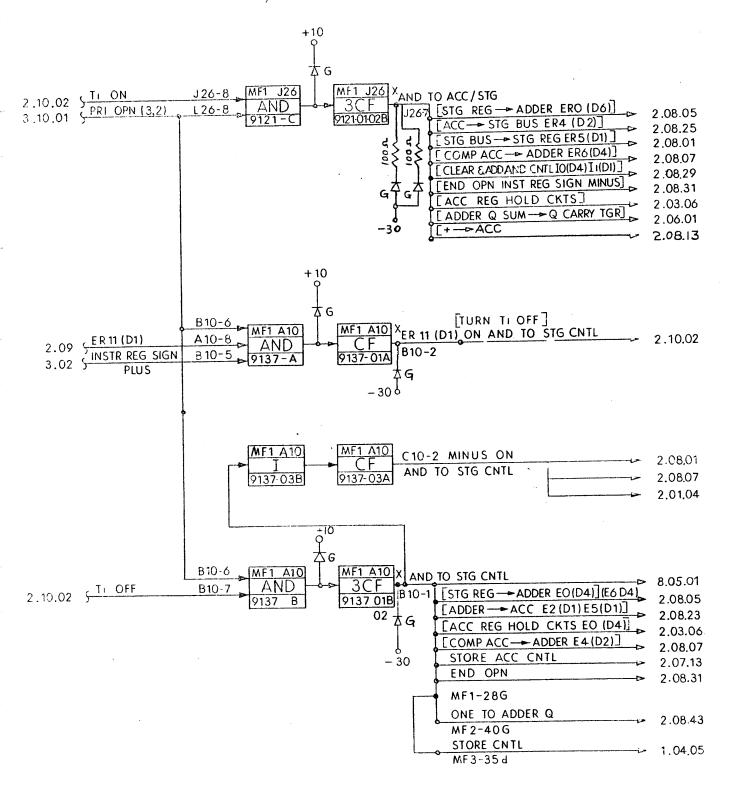


207.13

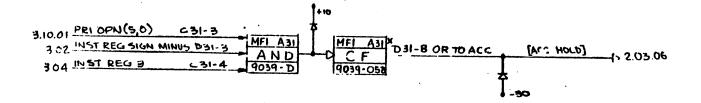






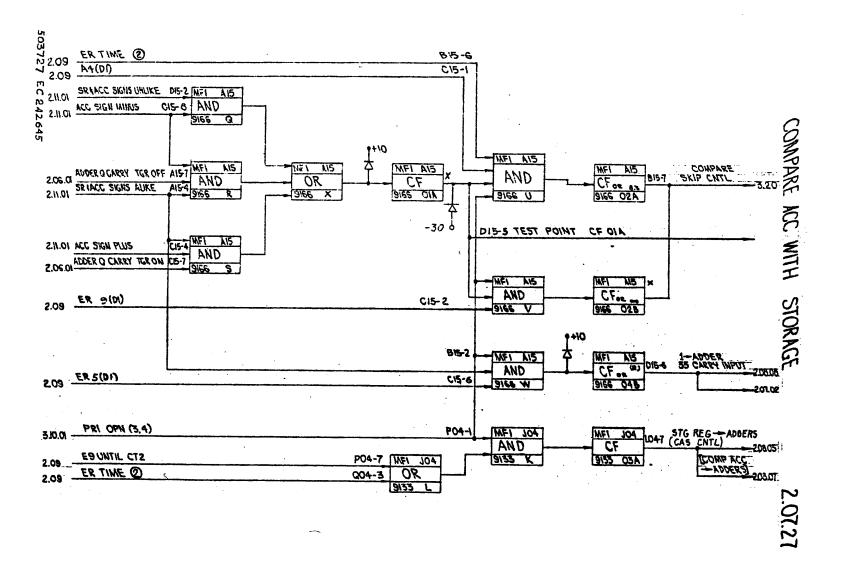


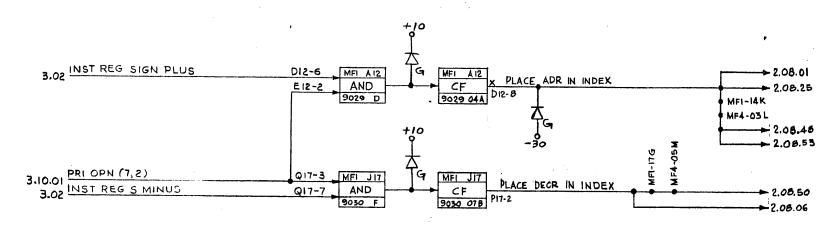
OR TO ACC EXEC CNTL 2.07.25

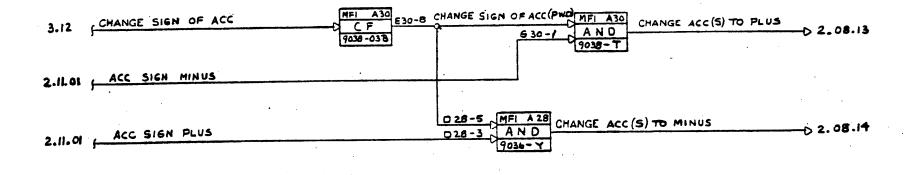


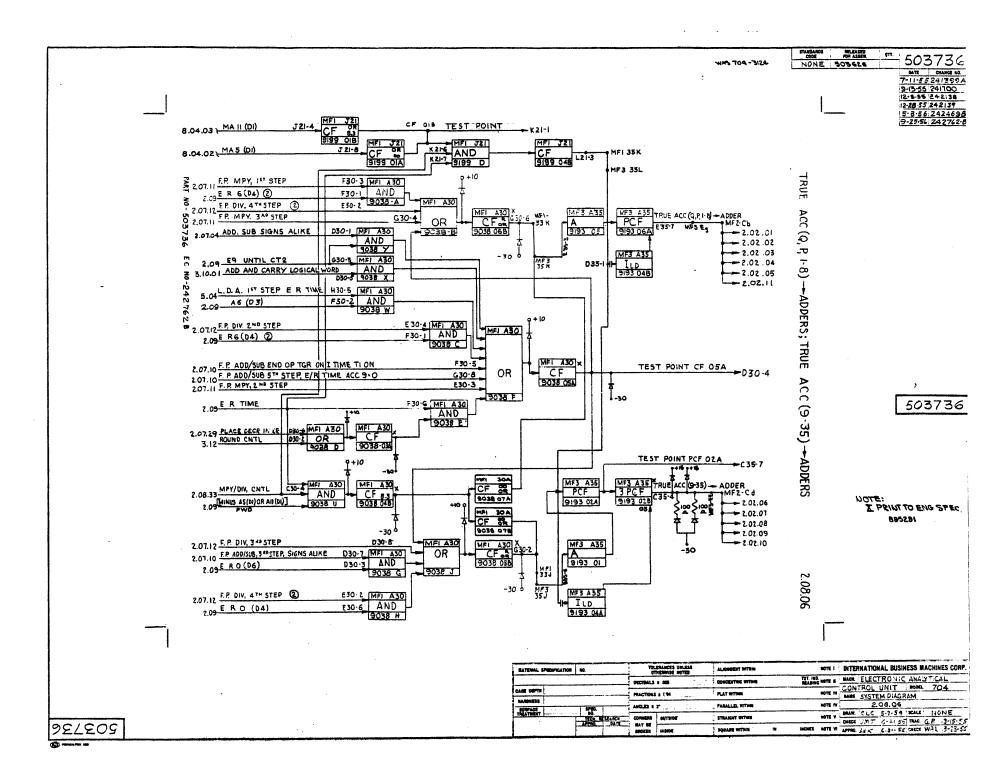
ACDER CNTL.

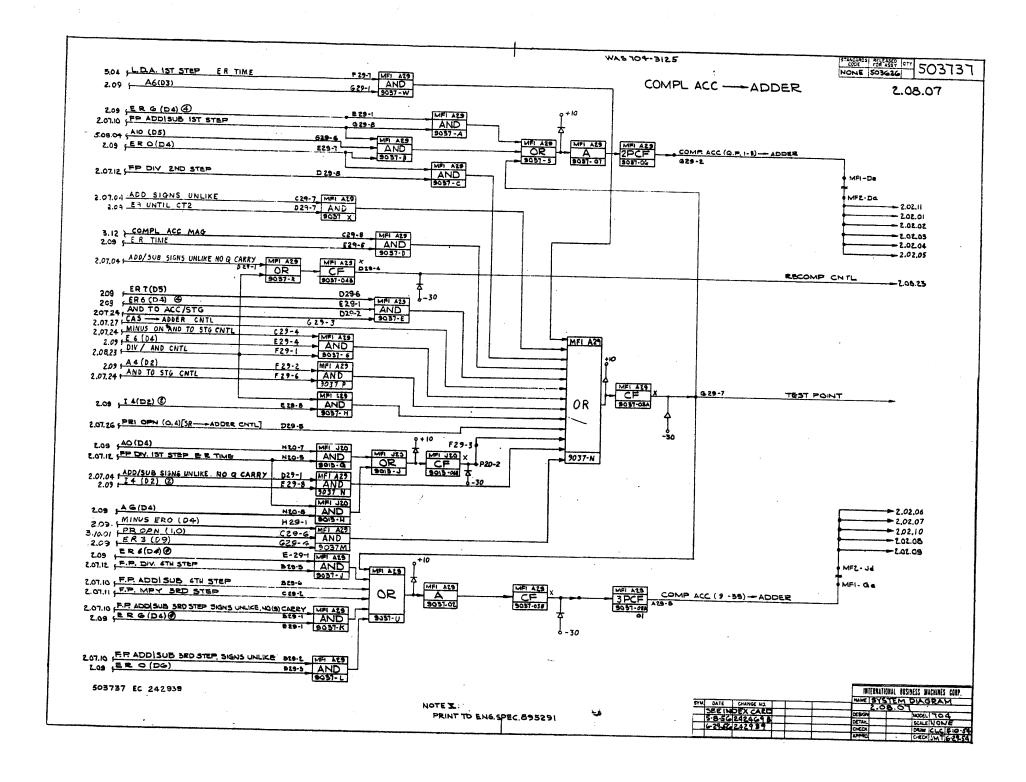
72 2.07.26

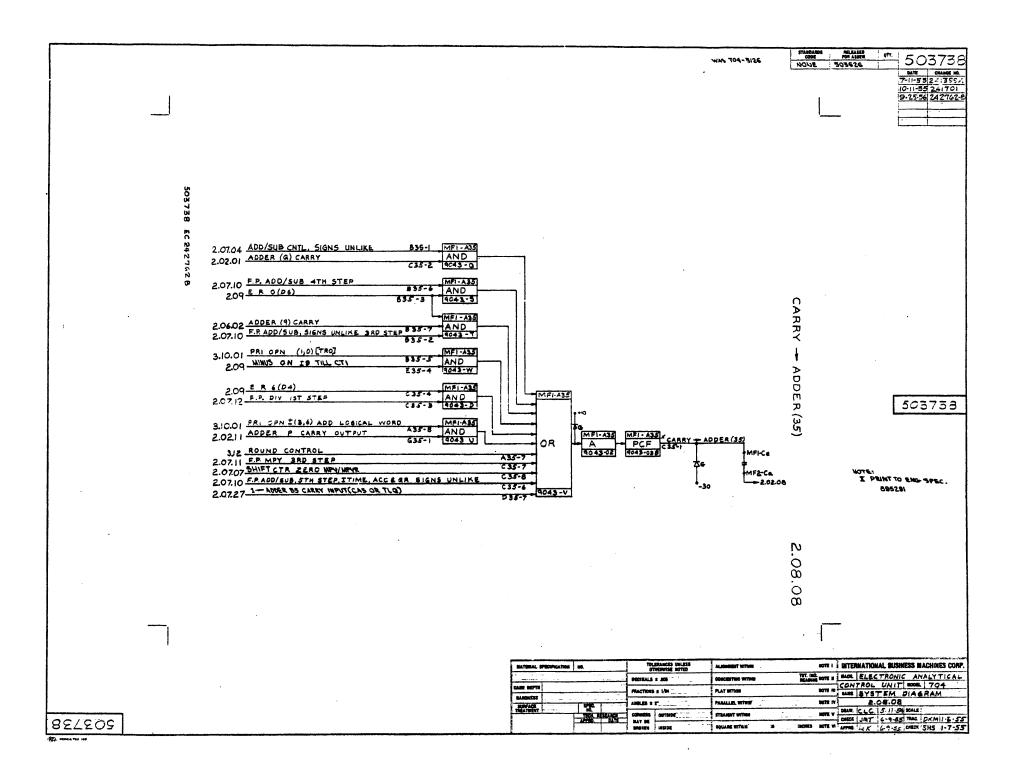






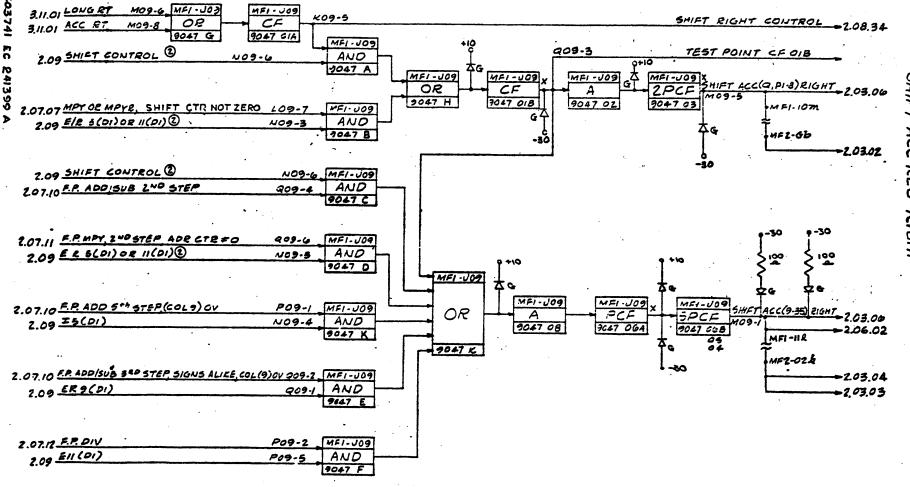


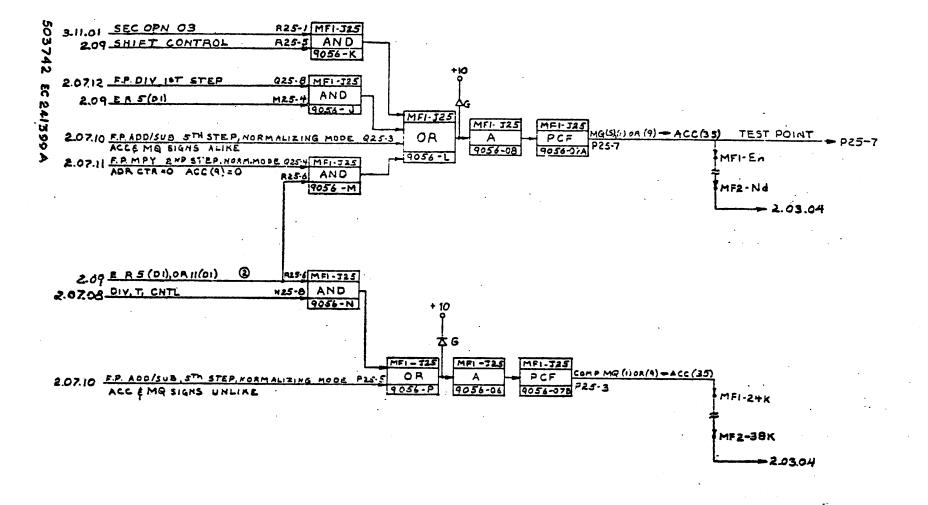


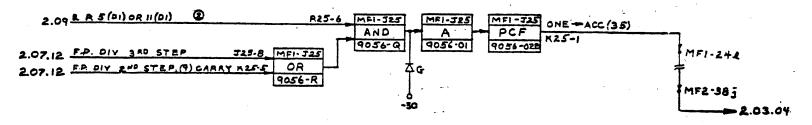


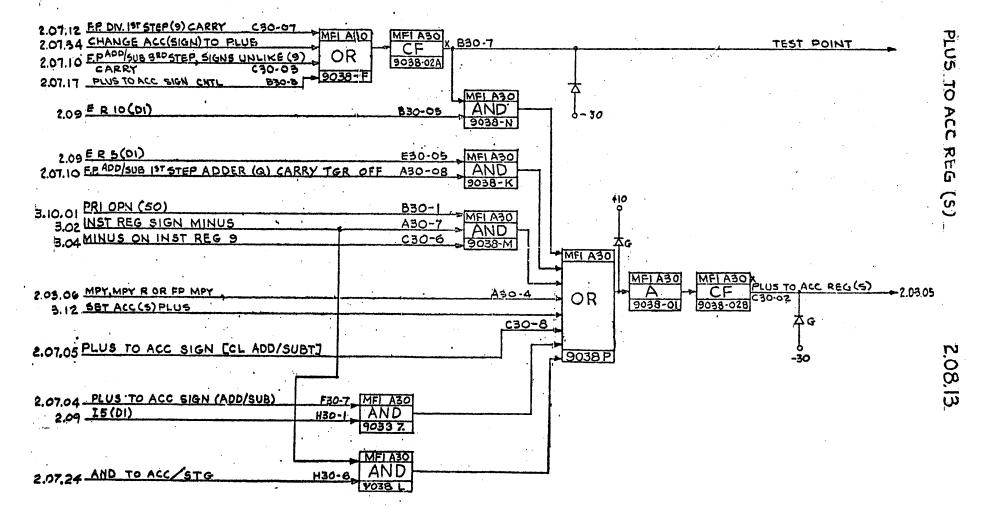
P17-1 AND N17-4 9030 E

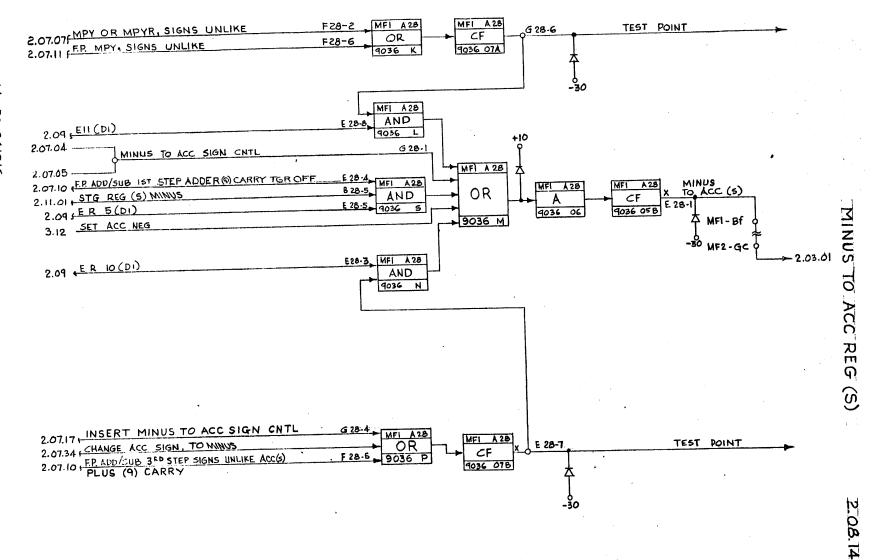
2.07.57 PLACE INDEX REG. IN DECR 2./2.02 TAG NOT ZERO 503739 EC 24276+-4











3.10.01 PRI OFN [56] LOAD MR REG 5.05.03 COPY WRITE

MQ

RI

2.09 EII (DI)

4.03 OP PNL

2.09 AG (D4)

MFI J31

CF 9054-08B

MFI J31

AND

9054 H

R31-1 9054-A

P 31-5

Q31-6

AND R31-5 9054-B

MFI J31

0+10

MFI J31 8PCF 9054-03 04 05

-**პ**0

ÅĢ

-30 9

MF1 J31

CF

9054 -07A

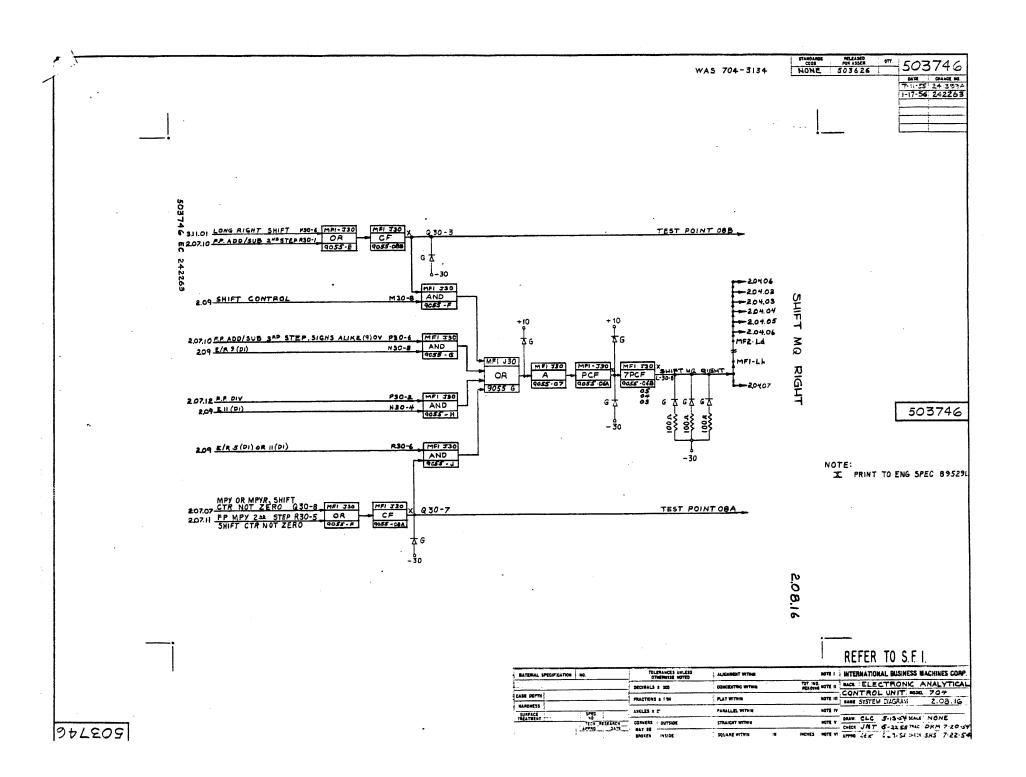
9+10

MFI J31

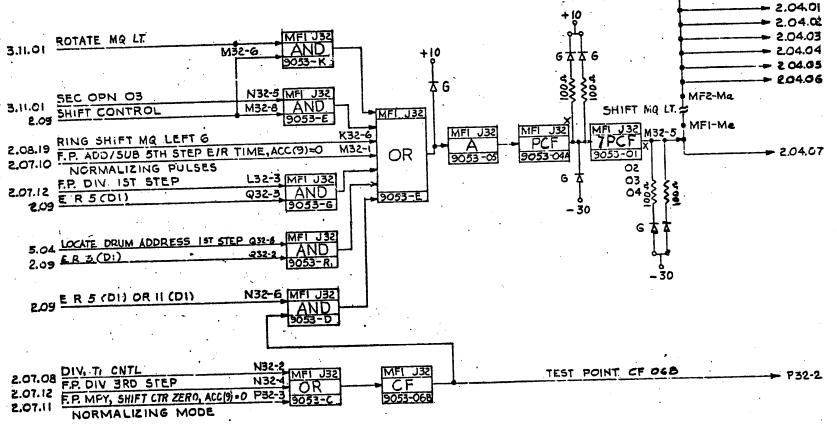
9054-07B

MFI J31

OR 9054-6



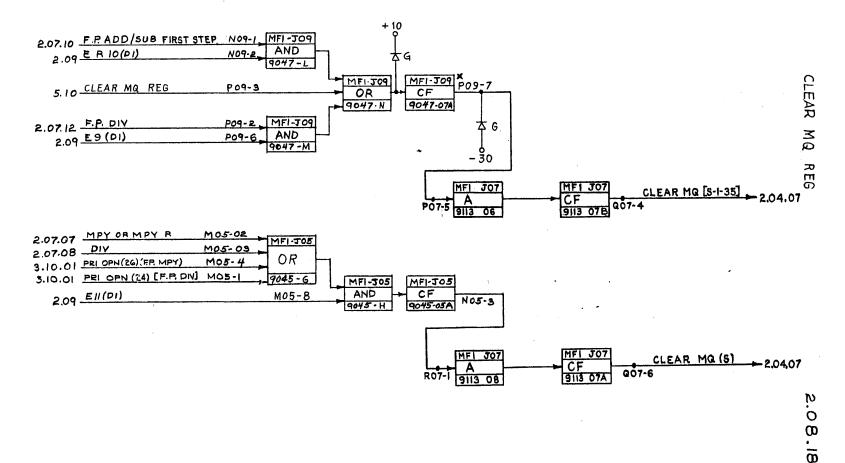
6.03.03

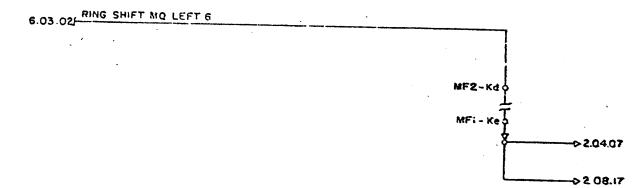


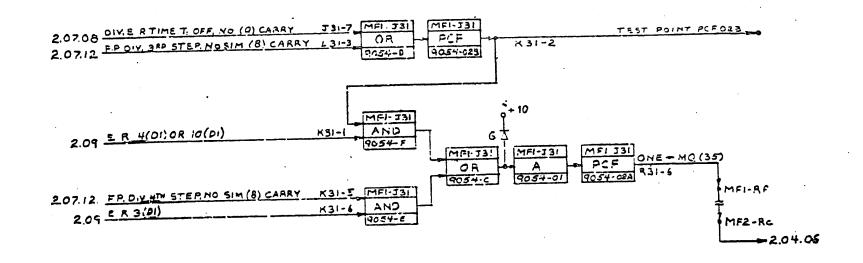
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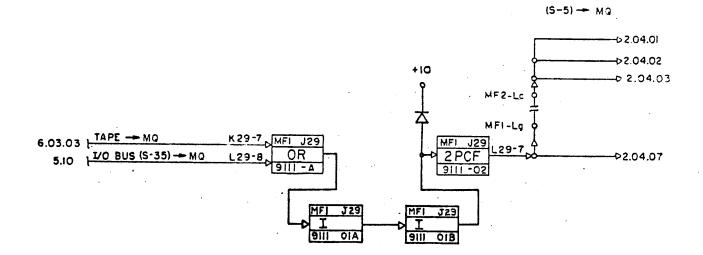
E

241787

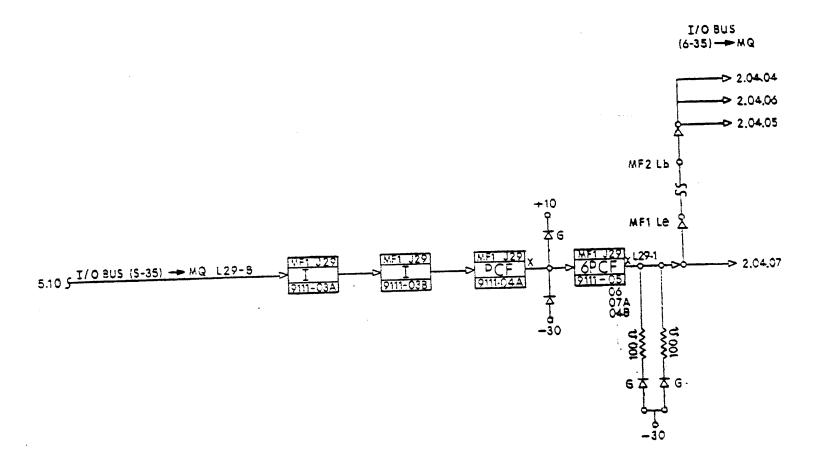


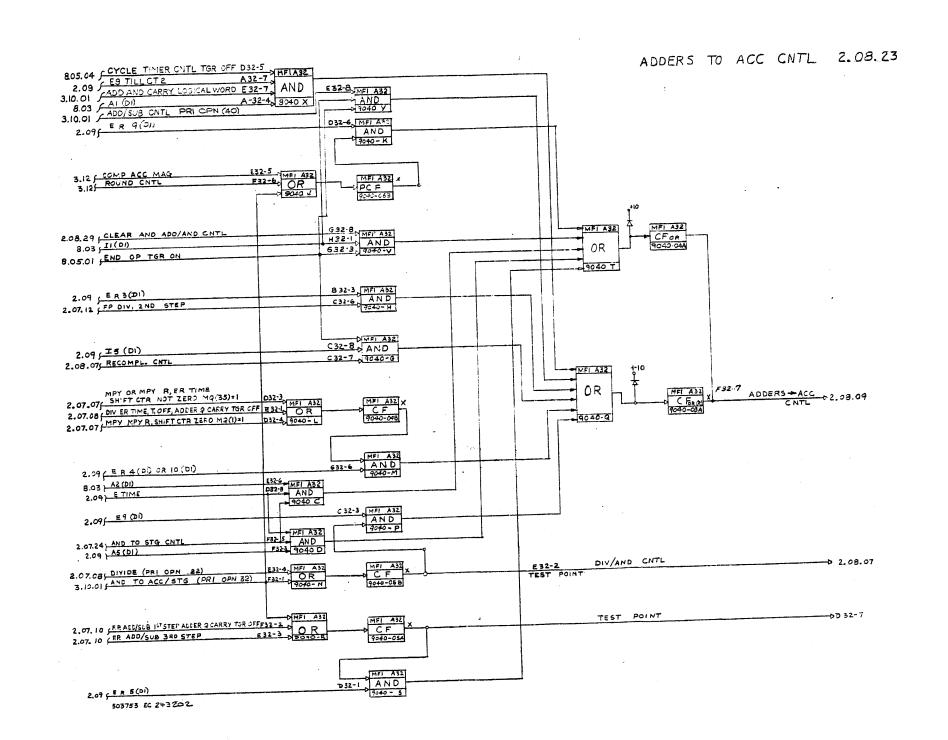


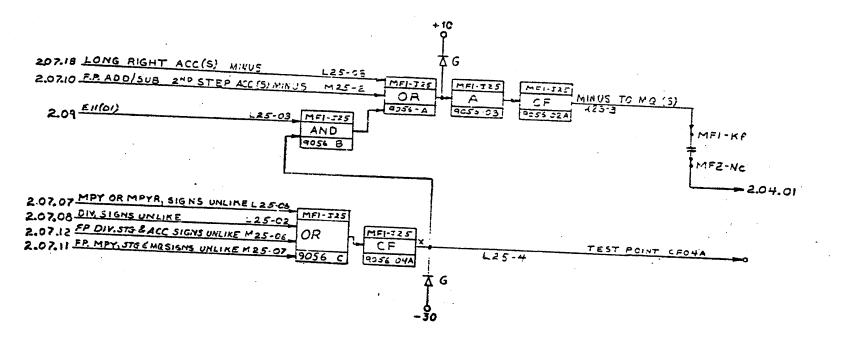


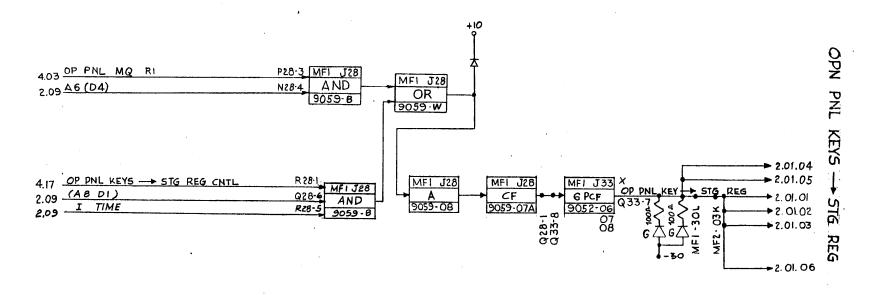


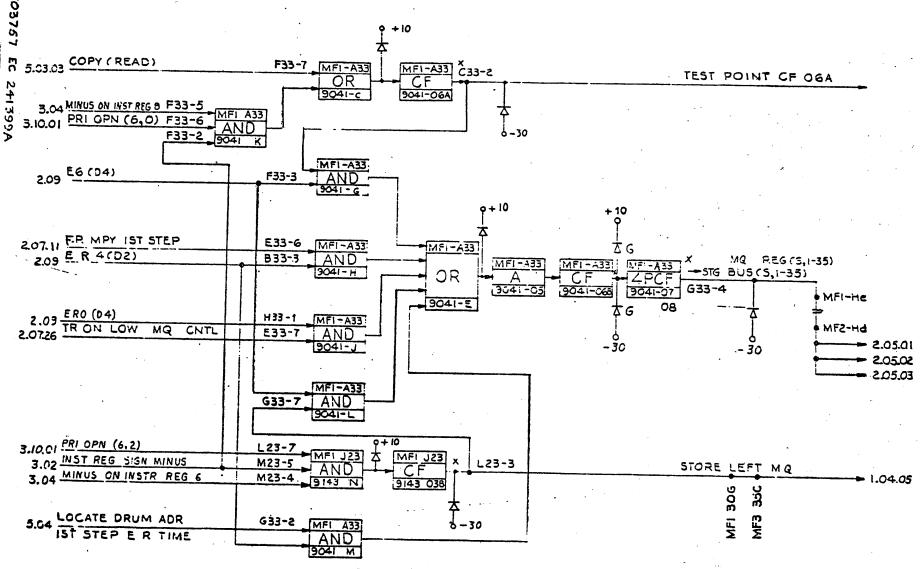
I/O BUS

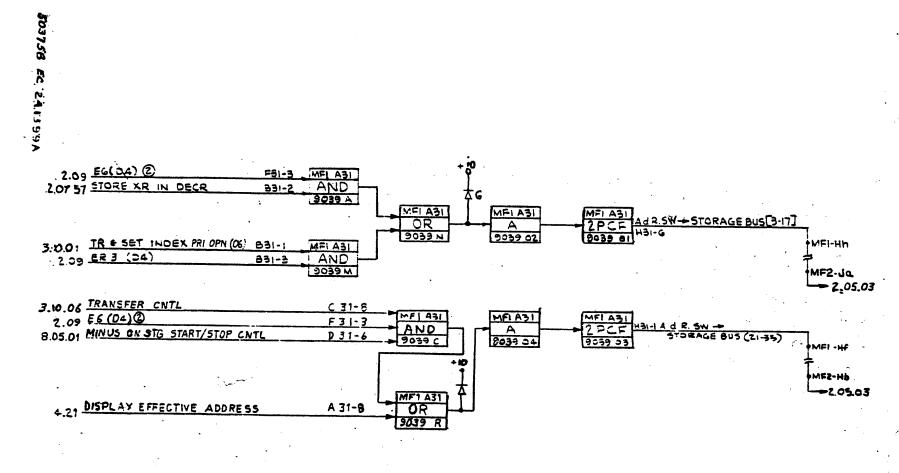


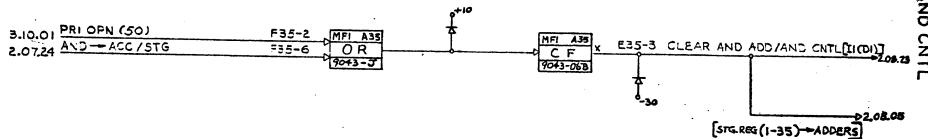


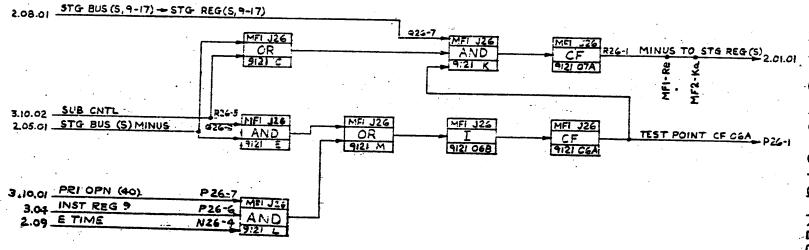


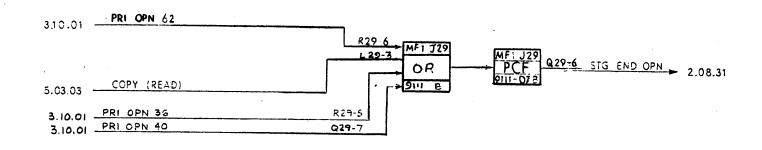


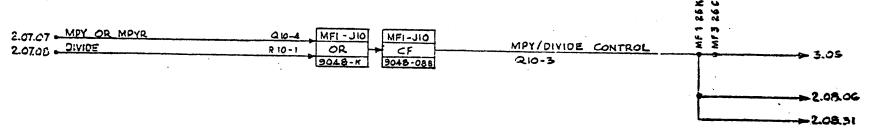


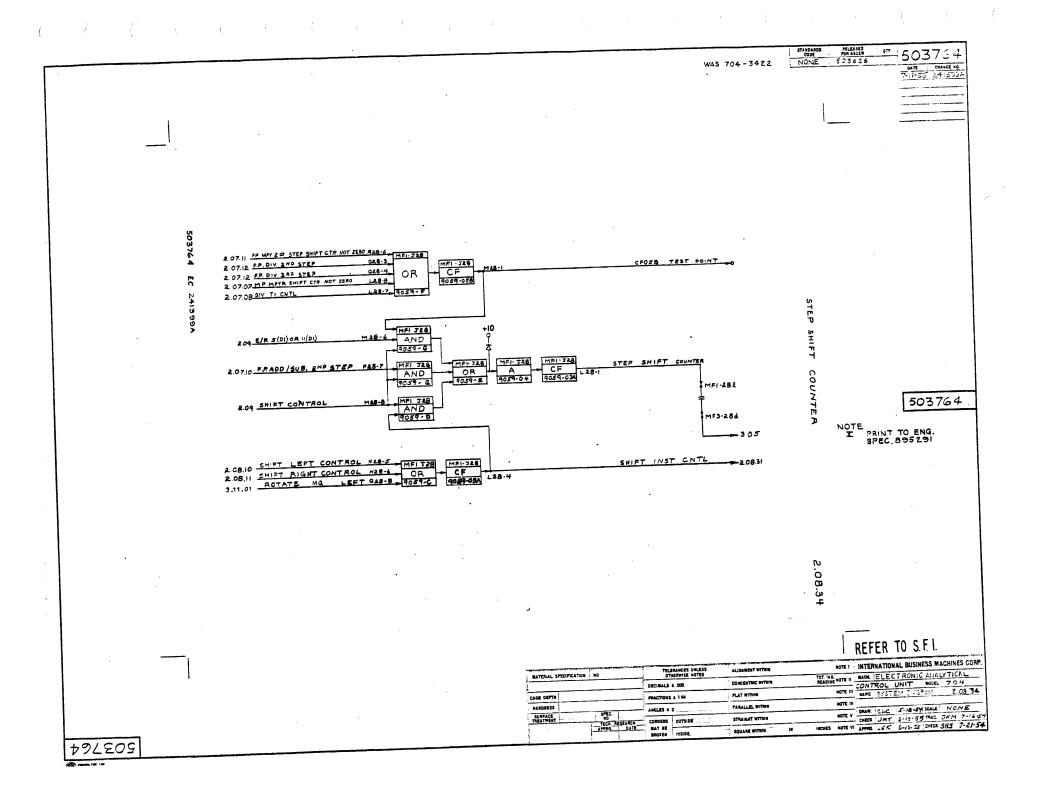


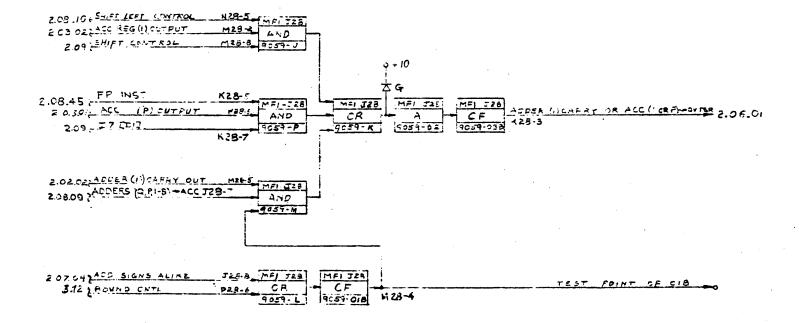


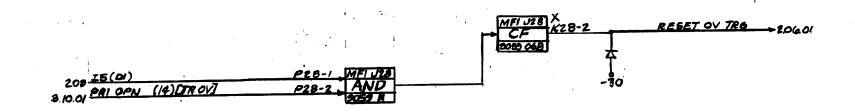


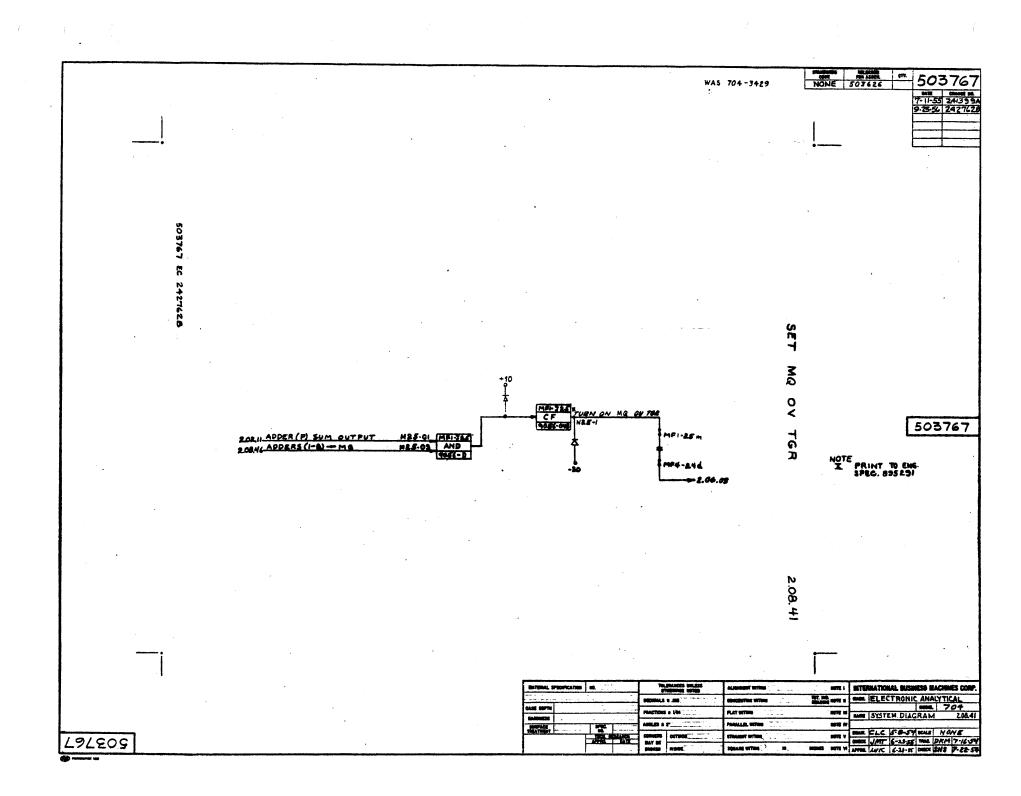


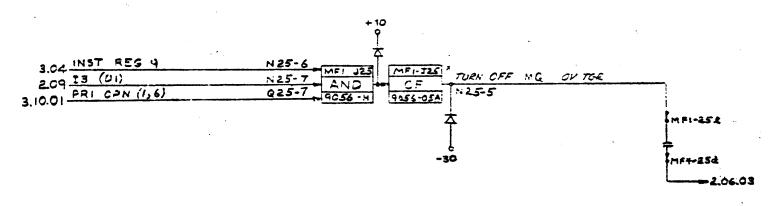


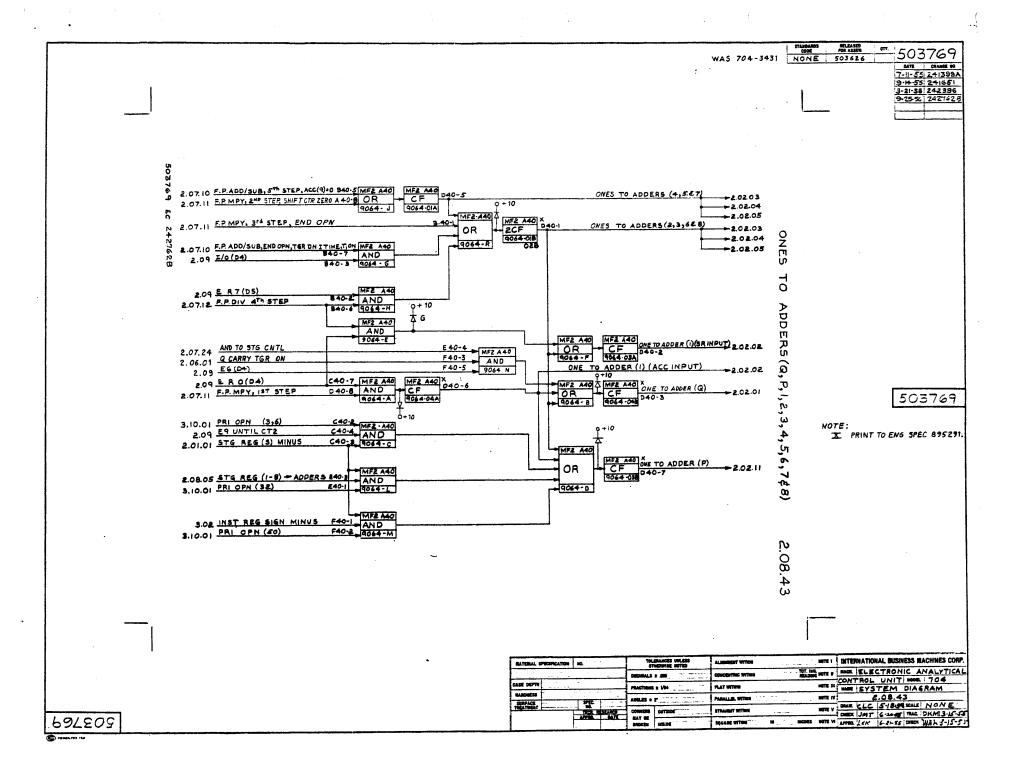


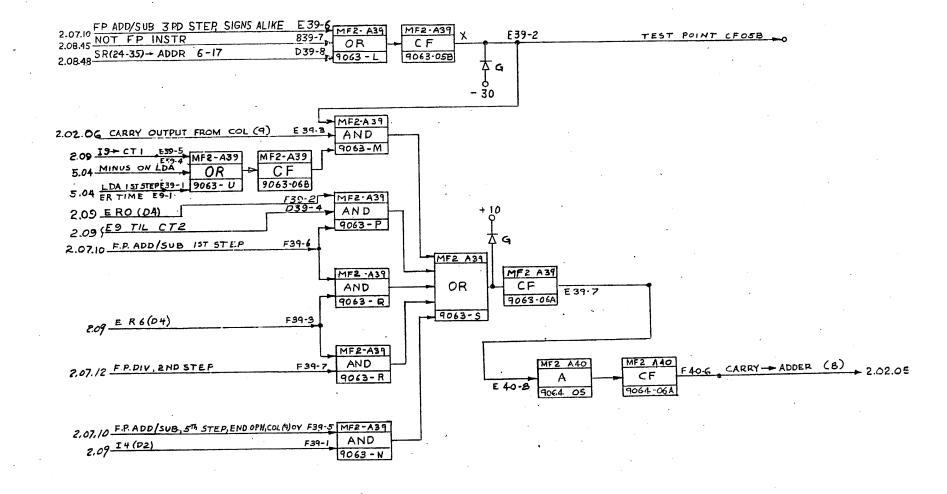


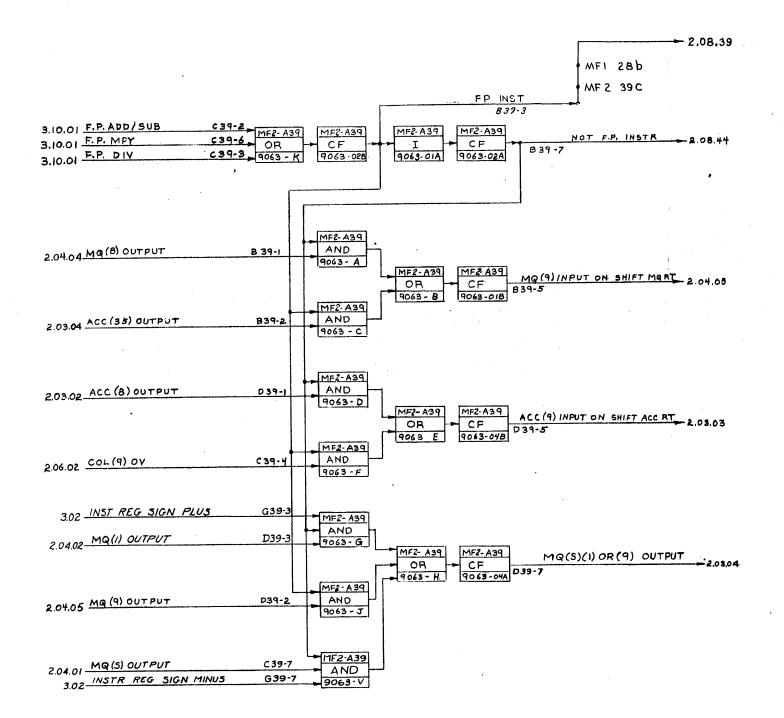


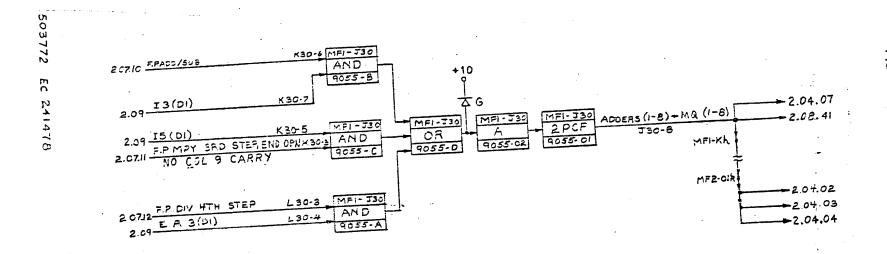


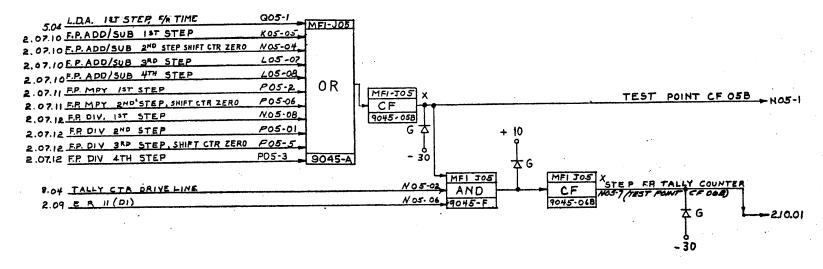




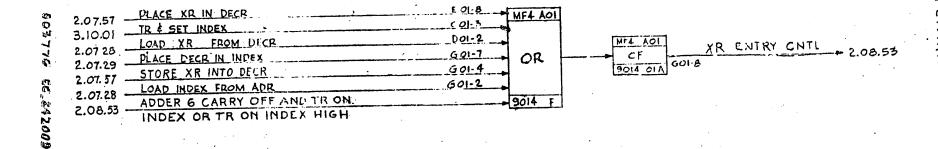


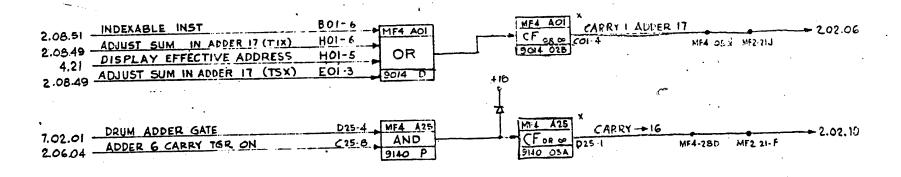






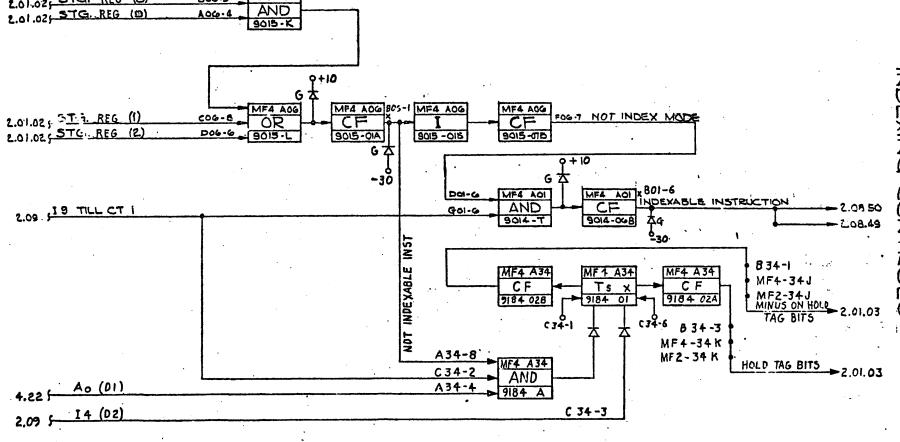
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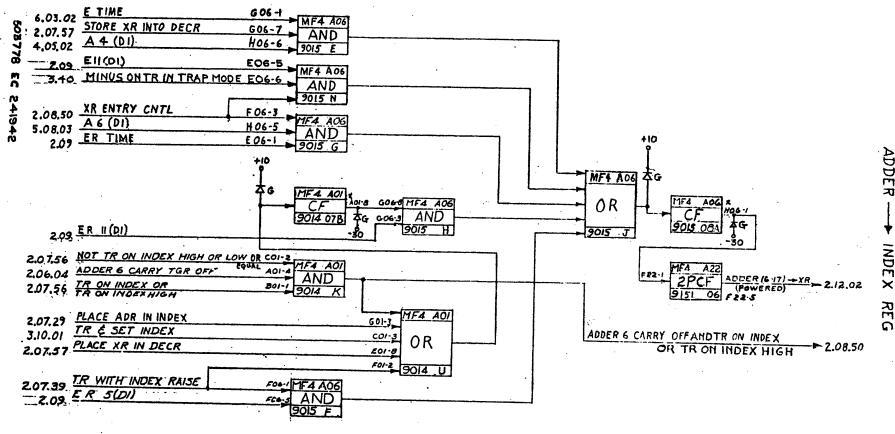


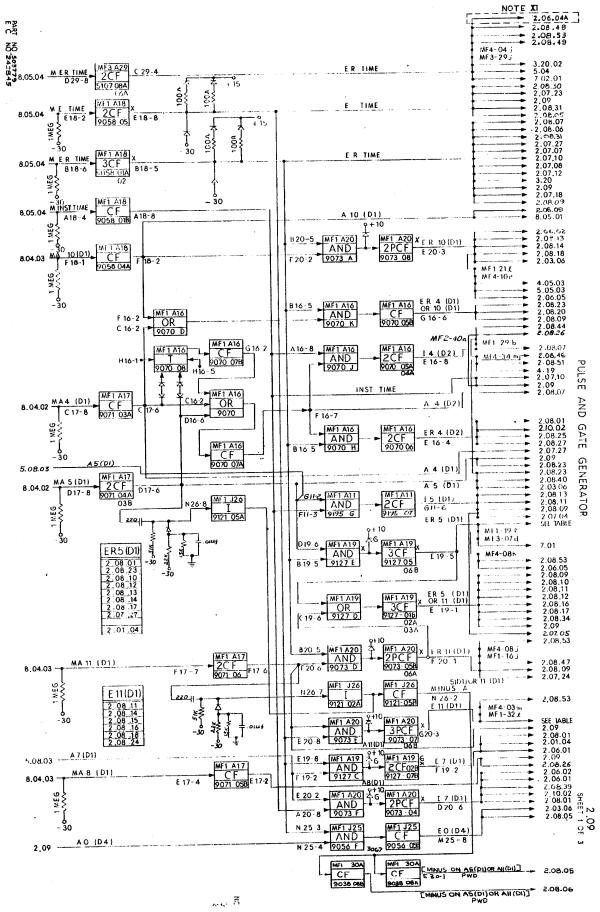


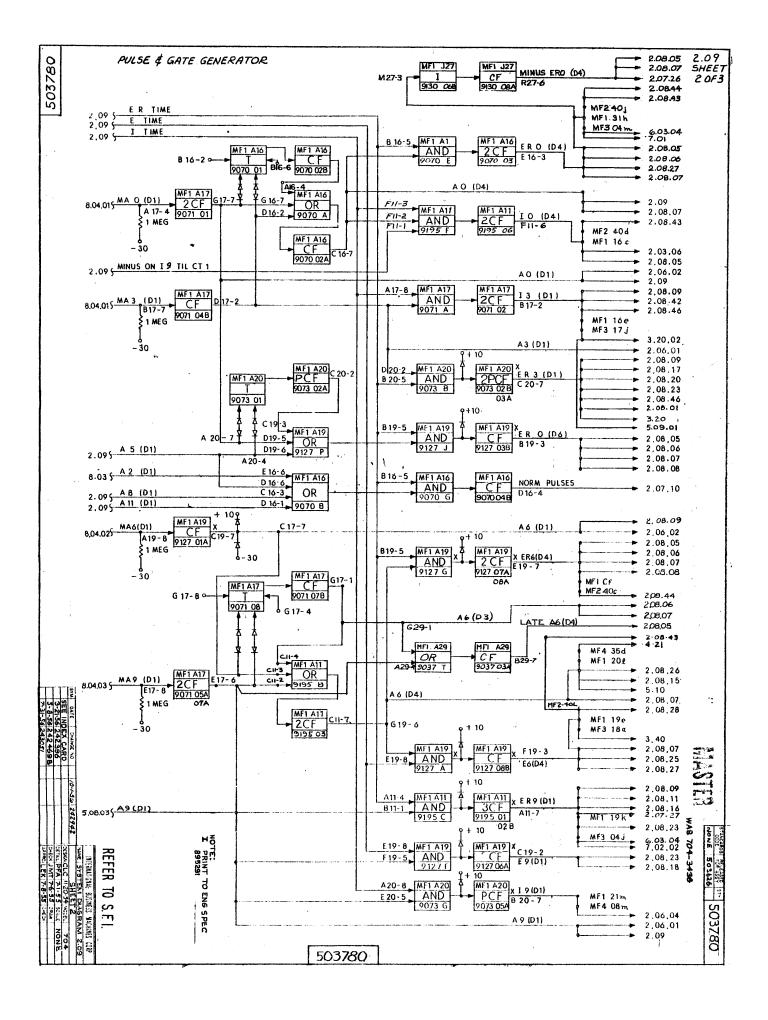
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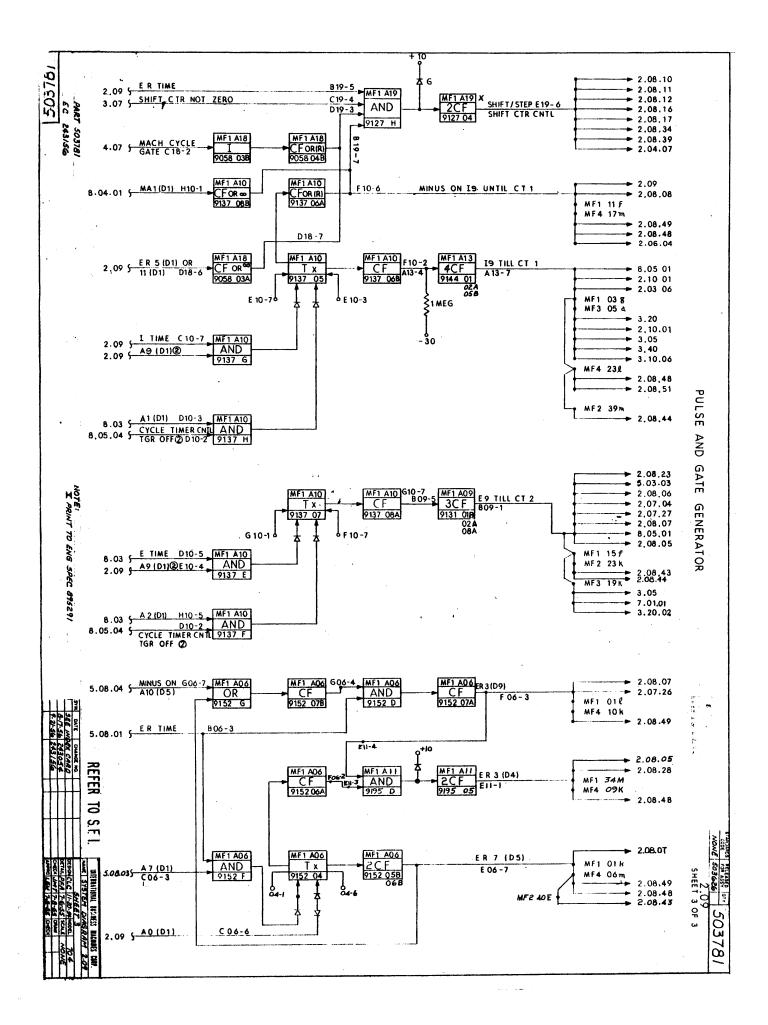
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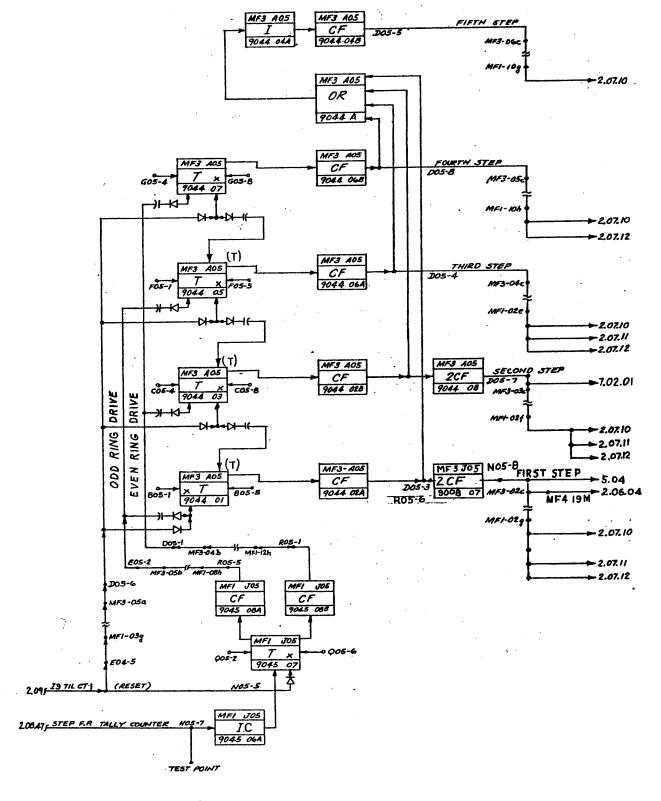




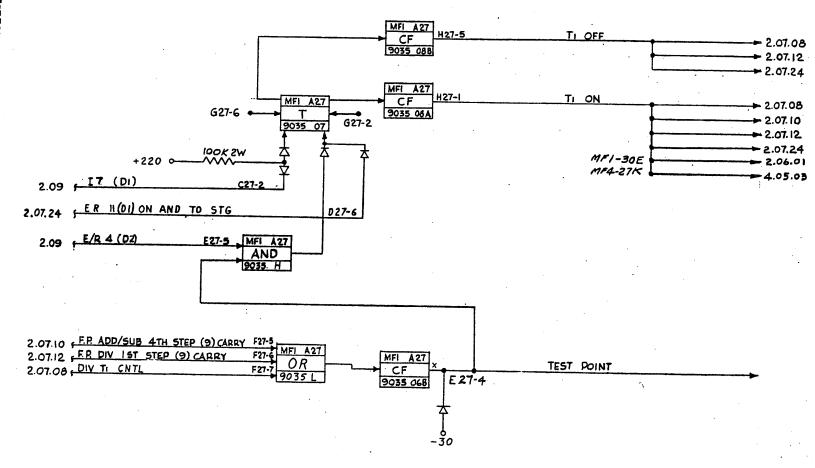


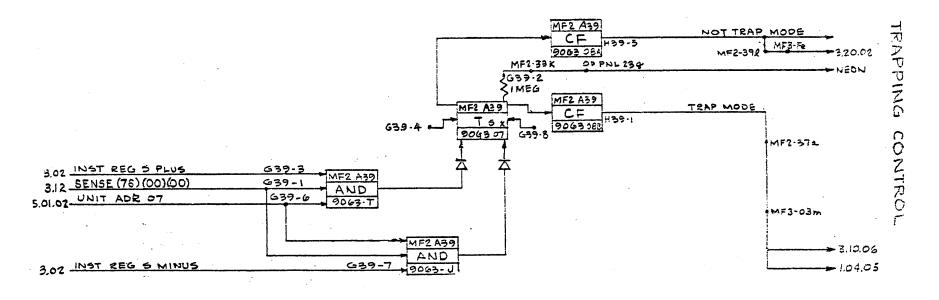


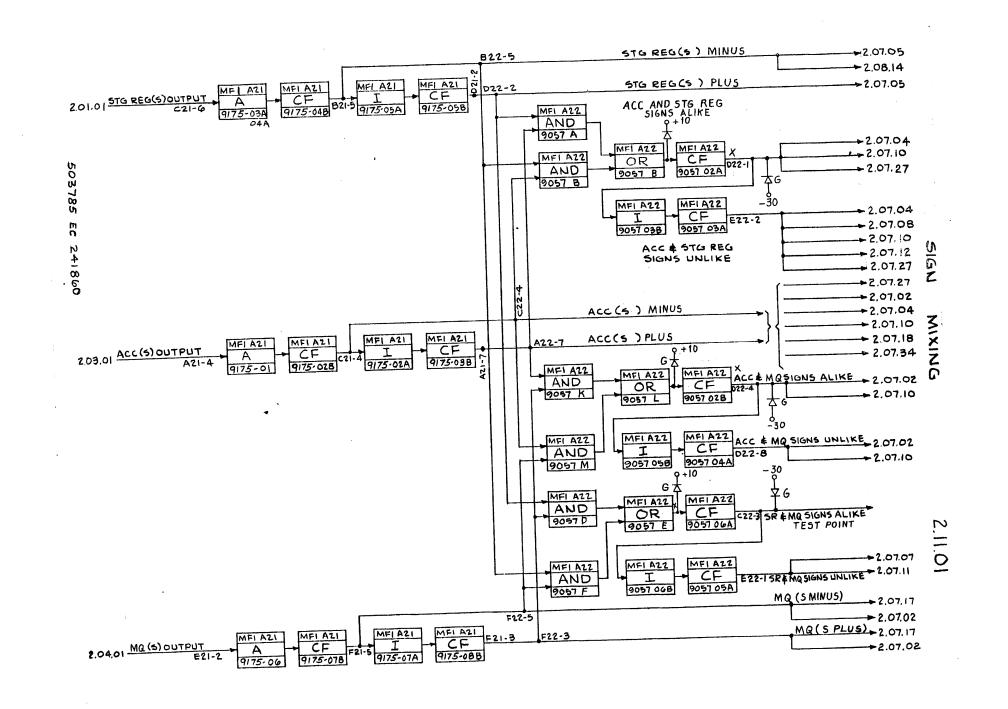


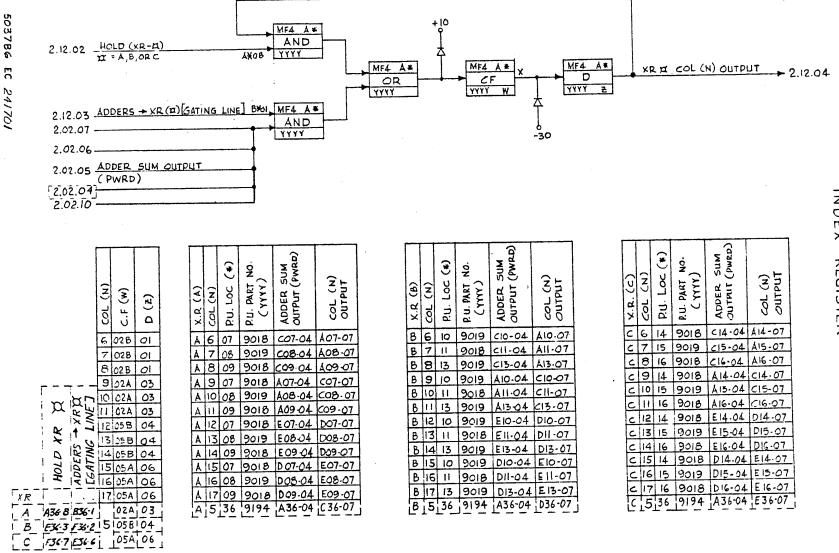


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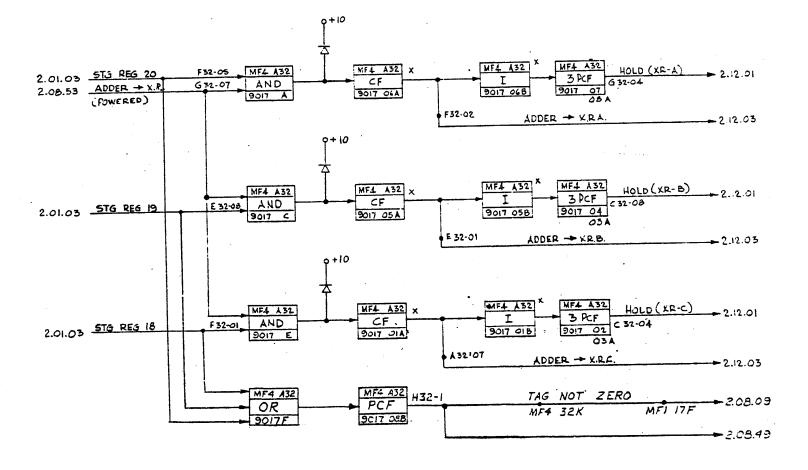


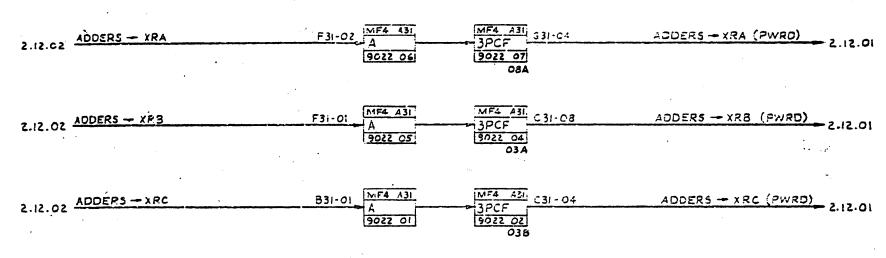


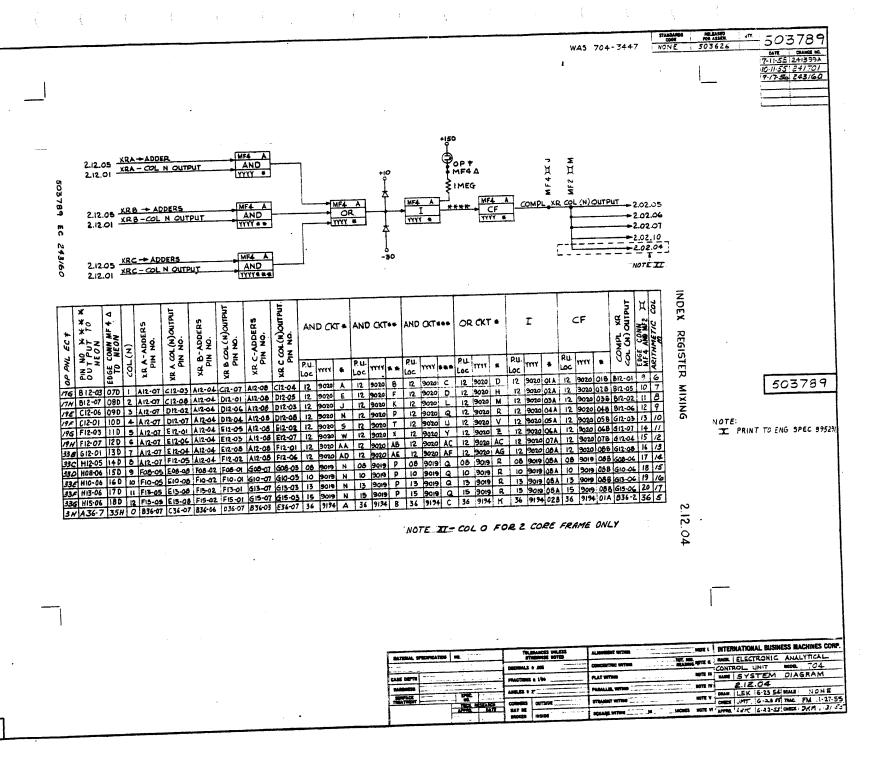




FOR 2 CORE FRAMES ONLY







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